Formal Methods

Module II: Formal Verification

Ch. 10: SMT-Based Model Checking

Roberto Sebastiani

DISI, Università di Trento, Italy - roberto.sebastiani@unitn.it URL: https://disi.unitn.it/rseba/DIDATTICA/fm2024/
Teaching assistant: Giuseppe Spallitta - giuseppe.spallitta@unitn.it

M.S. in Computer Science, Mathematics, & Artificial Intelligence Systems Academic year 2023-2024

last update: Friday 23rd February, 2024, 18:36

Copyright notice: some material (text, figures) displayed in these slides is courtesy of R. Alur, M. Benerecetti, A. Cimatti, M. Di Natale, P. Pandya, M. Pistore, M. Roveri, C. Tinelli, and S. Tonetta, who detain its copyright. Some exampes displayed in these slides are taken from [Clarke, Grunberg & Peled, "Model Checking", MIT Press], and their copyright is detained by the authors. All the other material is copyrighted by Roberto Sebastiani. Every commercial use of this material is strictly forbidden by the copyright laws without the authorization of the authors. No copy of these slides can be displayed in public without containing this copyright notice.

Outline

- Motivations & Context
- Background (from previous chapters)
- SMT-Based Bounded Model Checking of Timed Systems
 - Basic Ideas
 - Basic Encoding
 - Improved & Extended Encoding
 - A Case-Study
- SMT-Based Bounded Model Checking of Linear Hybrid Systems (hints)
- Proposed Exercises

Outline

- Motivations & Context
- Background (from previous chapters
- SMT-Based Bounded Model Checking of Timed Systems
 - Basic Ideas
 - Basic Encoding
 - Improved & Extended Encoding
 - A Case-Study
- SMT-Based Bounded Model Checking of Linear Hybrid Systems (hints)
- Proposed Exercises

- Model Checking for Timed Systems:
 - relevant improvements and results over the last decades
 - historically, "explicit-state" search style, based on DBMs
 - notable examples: Kronos, Uppaal
 - More recently, symbolic verification techniques:
 - extensions of decision diagrams
 - CDD, DDD, RED, ...
- Key problem: potential blow up in size
- A more recent and viable alternative to Binary Decision Diagrams: SAT-based MC
 - Bounded Model Checking (BMC), K-induction, IC3/PDR, ...

- Model Checking for Timed Systems:
 - relevant improvements and results over the last decades
 - historically, "explicit-state" search style, based on DBMs
 - notable examples: Kronos, Uppaal
 - More recently, symbolic verification techniques:
 - extensions of decision diagrams
 - CDD, DDD, RED, ...
- Key problem: potential blow up in size
- A more recent and viable alternative to Binary Decision Diagrams: SAT-based MC
 - Bounded Model Checking (BMC), K-induction, IC3/PDR, ...

- Model Checking for Timed Systems:
 - relevant improvements and results over the last decades
 - historically, "explicit-state" search style, based on DBMs
 - notable examples: Kronos, Uppaal
 - More recently, symbolic verification techniques:
 - extensions of decision diagrams
 - CDD, DDD, RED, ...
- Key problem: potential blow up in size
- A more recent and viable alternative to Binary Decision Diagrams: SAT-based MC
 - Bounded Model Checking (BMC), K-induction, IC3/PDR, ...

- Model Checking for Timed Systems:
 - relevant improvements and results over the last decades
 - historically, "explicit-state" search style, based on DBMs
 - notable examples: Kronos, Uppaal
 - More recently, symbolic verification techniques:
 - extensions of decision diagrams
 - CDD, DDD, RED, ...
- Key problem: potential blow up in size
- A more recent and viable alternative to Binary Decision Diagrams: SAT-based MC
 - Bounded Model Checking (BMC), K-induction, IC3/PDR, ...

First Idea: SMT-based BMC of Timed Systems

[Audemard et al. 2002], [Sorea, MTCS'02], [Niebert et al.,FTRTFT'02]

Leverage the SAT-based BMC approach to Timed Systems by means of SMT Solvers

Extensions

- SMT eventually applied to other SAT-based MC techniques
 - K-Induction
 - interpolant-based
 - IC3/PDR
- SMT applied to a variety of domains:
 - hvbrid systems
 - verification of SW (loop invariants/proof obbligations, ...
 - hardware verification
- Nowadays SMT leading backend technology for FV

First Idea: SMT-based BMC of Timed Systems

[Audemard et al. 2002], [Sorea, MTCS'02], [Niebert et al.,FTRTFT'02]

Leverage the SAT-based BMC approach to Timed Systems by means of SMT Solvers

Extensions

- SMT eventually applied to other SAT-based MC techniques
 - K-Induction
 - interpolant-based
 - IC3/PDR
- SMT applied to a variety of domains:
 - hybrid systems
 - verification of SW (loop invariants/proof obbligations, ...)
 - hardware verification
- Nowadays SMT leading backend technology for FV

First Idea: SMT-based BMC of Timed Systems

[Audemard et al. 2002], [Sorea, MTCS'02], [Niebert et al.,FTRTFT'02]

Leverage the SAT-based BMC approach to Timed Systems by means of SMT Solvers

Extensions

- SMT eventually applied to other SAT-based MC techniques
 - K-Induction
 - interpolant-based
 - IC3/PDR
- SMT applied to a variety of domains:
 - hybrid systems
 - verification of SW (loop invariants/proof obbligations, ...)
 - hardware verification
- Nowadays SMT leading backend technology for FV

First Idea: SMT-based BMC of Timed Systems

[Audemard et al. 2002], [Sorea, MTCS'02], [Niebert et al.,FTRTFT'02]

Leverage the SAT-based BMC approach to Timed Systems by means of SMT Solvers

Extensions

- SMT eventually applied to other SAT-based MC techniques
 - K-Induction
 - interpolant-based
 - IC3/PDR
- SMT applied to a variety of domains:
 - hybrid systems
 - verification of SW (loop invariants/proof obbligations, ...)
 - hardware verification
- Nowadays SMT leading backend technology for FV

First Idea: SMT-based BMC of Timed Systems

[Audemard et al. 2002], [Sorea, MTCS'02], [Niebert et al.,FTRTFT'02]

Leverage the SAT-based BMC approach to Timed Systems by means of SMT Solvers

Extensions

- SMT eventually applied to other SAT-based MC techniques
 - K-Induction
 - interpolant-based
 - IC3/PDR
- SMT applied to a variety of domains:
 - hybrid systems
 - verification of SW (loop invariants/proof obbligations, ...)
 - hardware verification
- Nowadays SMT leading backend technology for FV

Outline

- Motivations & Context
- Background (from previous chapters)
- SMT-Based Bounded Model Checking of Timed Systems
 - Basic Ideas
 - Basic Encoding
 - Improved & Extended Encoding
 - A Case-Study
- 4 SMT-Based Bounded Model Checking of Linear Hybrid Systems (hints)
- Proposed Exercises

Bounded Model Checking [Biere et al., TACAS'99]

- Given a Kripke Structure M, an LTL property f and an integer bound k, is there an execution path of M of length (up to) k satisfying f? ($M \models_k Ef$)
- Problem converted into the satisfiability of the Boolean formula:

$$[[M]]_{k}^{f} := I(s^{(0)}) \wedge \bigwedge_{i=0}^{k-1} R(s^{(i)}, s^{(i+1)}) \wedge (\neg L_{k} \wedge [[f]]_{k}^{0}) \vee \bigvee_{l=0}^{k} ({}_{l}L_{k} \wedge {}_{l}[[f]]_{k}^{0})$$

s.t.
$$_{l}L_{k}\stackrel{\text{def}}{=} R(s^{(k)}, s^{(l)}), L_{k}\stackrel{\text{def}}{=} \bigvee_{l=0}^{k} {}_{l}L_{k}$$

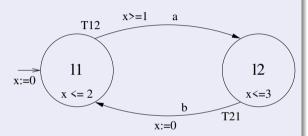
- A satisfying assignment represents a satisfying execution path.
- Test repeated for increasing values of k
- Incomplete
- Very effective for debugging, alternative to OBDDs
- Complemented with K-Induction [Sheeran et al. 2000]
- Further developments: IC3/PDR [Bradley, VMCAI 2011]

General Encoding for LTL Formulae

f	$[[f]]_k^i$	$I[[f]]_k^I$
р	$p^{(i)}$	$\rho^{(i)}$
$\neg p$	$\neg p^{(i)}$	$\neg p^{(i)}$
$h \wedge g$	$[[h]]_k^i \wedge [[g]]_k^i$	$I_{i}[[h]]_{k}^{i} \wedge I_{i}[[g]]_{k}^{i}$
h∨g	$[[h]]_k^{\hat{i}} \vee [[g]]_k^{\hat{i}}$	$I_{[[h]]_{K}^{i}} \vee I_{[[g]]_{K}^{i}}$
Х g	$ [g] _k^{i+1}$ if $i < k$	$\int_{I} [[g]]_{k}^{i+1}$ if $i < k$
	\perp otherwise.	$_{I}[[g]]_{k}^{T}$ otherwise.
G g	上	$\bigwedge_{j=\min(i,l)}^{k} I[[g]]_{k}^{j}$
F g	$\bigvee_{j=i}^{k} [[g]]_{k}^{j}$	$\bigvee_{j=\min(i,l)}^{k} {}_{l}[[g]]_{k}^{j}$
h U g	$\bigvee_{j=i}^{k} \left([[g]]_{k}^{j} \wedge \bigwedge_{n=i}^{j-1} [[h]]_{k}^{n} \right)$	$\bigvee_{j=i}^{k} \left({}_{I}[[g]]_{k}^{j} \wedge \bigwedge_{n=i}^{j-1} {}_{I}[[h]]_{k}^{n} \right) \vee$
	, , , , , , , , , , , , , , , , , , ,	$\bigvee_{j=1}^{i-1} \left({}_{i}[[g]]_{k}^{j} \wedge \bigwedge_{n=i}^{k} {}_{i}[[h]]_{k}^{n} \wedge \bigwedge_{n=i}^{j-1} {}_{i}[[h]]_{k}^{n} \right)$
h R g	$\bigvee_{j=i}^{k} \left([[h]]_{k}^{j} \wedge \bigwedge_{n=i}^{j} [[g]]_{k}^{n} \right)$	$\bigwedge_{j=\min(i,l)}^k [g]]_k^j \vee$
		$\bigvee_{j=i}^{k} \left({}_{l}[[h]]_{k}^{j} \wedge \bigwedge_{n=i}^{j} {}_{l}[[g]]_{k}^{n} \right) \vee$
		$\bigvee_{j=1}^{i-1} \left({}_{I}[[h]]_{k}^{j} \wedge \bigwedge_{n=i}^{k} {}_{I}[[g]]_{k}^{n} \wedge \bigwedge_{n=i}^{j} {}_{I}[[g]]_{k}^{n} \right)$

Timed Automata [Alur and Dill, TCS'94; Alur, CAV'99]

- Clocks: real variables (ex. x)
- Locations:
 - label: (ex. *l*₁),
 - invariants: (conjunctive) constraints on clocks values (ex. $x \le 2$)
- Switches:
 - event labels (ex. a),
 - clock constraints (ex. $x \ge 1$),
 - reset statements (ex. x := 0)
- Time elapse: all clocks are increased by the same amount



\mathcal{LRA} -Formulae

[Audemard et al., CADE'02]; [Sorea, MTCS'02]; [Niebert et al.,FTRTFT'02]

- LRA-formulae are Boolean combinations of
 - Boolean variables and
 - linear constraints over real variables (equalities and differences)

• e.g.,
$$(x - 2 \cdot y \ge 4) \land ((x = y) \lor \neg A)$$

- An interpretation \mathcal{I} for a \mathcal{LRA} formula assigns
 - truth values to Boolean variables
 - real values to numerical variables and constants

• e.g.,
$$\mathcal{I}(x) = 3$$
, $\mathcal{I}(y) = -1$, $\mathcal{I}(A) = \bot$

- \mathcal{I} satisfies a \mathcal{LRA} -formula ϕ , written " $\mathcal{I} \models \phi$ ", iff $\mathcal{I}(\phi)$ evaluates to true under the standard semantics of Boolean and mathematical operators.
 - E.g., $\mathcal{I}((x-2 \cdot y \ge 4) \wedge ((x=y) \vee \neg A)) = \top$



The MATHSAT Solver [Audemard et al., CADE'02]

- Bottom level: a \mathcal{T} -Solver for sets of \mathcal{LRA} constraints
 - E.g. $\{..., z_1 x_1 \le 6, z_2 x_2 \ge 8, x_1 = x_2, z_1 = z_2, ...\} \Longrightarrow unsat.$
 - Combination of symbolic and numerical algorithms (equivalence class building, Belman-Ford, Simplex)
- Top level: a CDCL procedure for propositional satisfiability
 - mathematical predicates treated as propositional atoms
 - ullet invokes $\mathcal{T} ext{-Solver}$ on every assignment found
 - used as an enumerator of assignments
 - lots of enhancements

(see chapter on SMT)

Outline

- Motivations & Context
- Background (from previous chapters)
- SMT-Based Bounded Model Checking of Timed Systems
 - Basic Ideas
 - Basic Encoding
 - Improved & Extended Encoding
 - A Case-Study
- 4 SMT-Based Bounded Model Checking of Linear Hybrid Systems (hints)
- Proposed Exercises

Outline

- Motivations & Context
- Background (from previous chapters
- SMT-Based Bounded Model Checking of Timed Systems
 - Basic Ideas
 - Basic Encoding
 - Improved & Extended Encoding
 - A Case-Study
- 4 SMT-Based Bounded Model Checking of Linear Hybrid Systems (hints)
- Proposed Exercises



SMT-Based BMC for Timed Systems

Independently developed approaches (2002):

- [Audemard et al. FORTE'02]: encoding into LRA
 - all LTL properties
- [Sorea, MTCS'02]: encoding into LRA
 - based on automata-theoretic approach for LTL
- Niebert et al.,FTRTFT'02]: encoding into DL
 - limited to reachability

Disclaimer

These slides are adapted from [Audemard et al. FORTE'02]:

G. Audemard, A. Cimatti, A. Kornilowicz, R. Sebastiani

Bounded Model Checking for Timed Systems,

proc. FORTE 2002, Springer

freely available as https://disi.unitn.it/rseba/pu

with some simplification in the notation).

SMT-Based BMC for Timed Systems

Independently developed approaches (2002):

- [Audemard et al. FORTE'02]: encoding into LRA
 - all LTL properties
- [Sorea, MTCS'02]: encoding into LRA
 - based on automata-theoretic approach for LTL
- Niebert et al.,FTRTFT'02]: encoding into DL
 - limited to reachability

Disclaimer

These slides are adapted from [Audemard et al. FORTE'02]:

G. Audemard, A. Cimatti, A. Kornilowicz, R. Sebastiani
Bounded Model Checking for Timed Systems,
proc. FORTE 2002, Springer
freely available as https://disi.unitn.it/rseba/publist.html

(with some simplification in the notation).

BMC for Timed Systems

Basic ingredients:

- An extension of propositional logic expressive enough to represent timed information: " \mathcal{LRA} -formulae"
- A SMT(\mathcal{LRA}) solver for deciding \mathcal{LRA} -formulae \Longrightarrow e.g., the MATHSAT solver
- \bullet An encoding from timed BMC problems into $\mathcal{LRA}\text{-}formulae$
 - ullet \mathcal{LRA} -satisfiable iff an execution path within the bound exists

BMC for Timed Systems

Basic ingredients:

- An extension of propositional logic expressive enough to represent timed information:
 "LRA-formulae"
- A SMT(\mathcal{LRA}) solver for deciding \mathcal{LRA} -formulae \Rightarrow e.g., the MATHSAT solver
- \bullet An encoding from timed BMC problems into $\mathcal{LRA}\text{-}formulae$
 - ullet \mathcal{LRA} -satisfiable iff an execution path within the bound exists

BMC for Timed Systems

Basic ingredients:

- An extension of propositional logic expressive enough to represent timed information:
 "LRA-formulae"
- A SMT(\mathcal{LRA}) solver for deciding \mathcal{LRA} -formulae \Longrightarrow e.g., the MATHSAT solver
- ullet An encoding from timed BMC problems into \mathcal{LRA} -formulae
 - ullet \mathcal{LRA} -satisfiable iff an execution path within the bound exists

Outline

- Motivations & Context
- Background (from previous chapters)
- SMT-Based Bounded Model Checking of Timed Systems
 - Basic Ideas
 - Basic Encoding
 - Improved & Extended Encoding
 - A Case-Study
- 4 SMT-Based Bounded Model Checking of Linear Hybrid Systems (hints)
- Proposed Exercises



The encoding

Given a timed automaton A and a LTL formula f:

• The encoding $[[A, f]]_k$ is obtained following the same schema as in propositional BMC:

$$[[A, f]]_k := I(s^{(0)}) \wedge \bigwedge_{i=0}^{k-1} R(s^{(i)}, s^{(i+1)}) \wedge (\neg L_k \wedge [[f]]_k^0) \vee \bigvee_{l=0}^k ({}_{l}L_k \wedge {}_{l}[[f]]_k^0)$$

- $[[M, f]]_k$ is a \mathcal{LRA} -formula, where
 - Boolean variables encode the discrete part of the state of the automaton
 - constraints on real variables represent the temporal part of the state

- Locations: an array \underline{I} of $n \stackrel{\text{def}}{=} \lceil log_2(|L|) \rceil$ Boolean variables
 - I_i holds iff the system is in the location I_i
 - ex: " $\neg \underline{l_i}[3] \land \underline{l_i}[2] \land \neg \underline{l_i}[1] \land \underline{l_i}[0]$ " means "the system is in location $\underline{l_5}$ "
 - " $(\underline{l_i} = \overline{l_j})$ " stands for " $\bigwedge_n (\underline{l_i}[n] \leftrightarrow l_j[n])$ ",
 - "primed" variables $\underline{l_i}$ to represent location after transition
- Events: for each event $a \in \Sigma$, a Boolean variable <u>a</u>
 - <u>a</u> holds iff the system executes a switch with event a.
- Switches: for each switch $\langle I_i, a, \varphi, \lambda, I_j \rangle \in E$, a Boolean variable T,
 - T holds iff the system executes the corresponding switch
- Time elapse and null transitions: two variables T_{δ} and T_{null}^{j}
 - T_{δ} holds iff time elapses by some $\delta > 0$
 - T_{null}^{j} holds if and only A_{j} does nothing (specific for automaton A_{j})

- Locations: an array \underline{I} of $n \stackrel{\text{def}}{=} \lceil log_2(|L|) \rceil$ Boolean variables
 - I_i holds iff the system is in the location I_i
 - ex: " $\neg l_i[3] \wedge l_i[2] \wedge \neg l_i[1] \wedge l_i[0]$ " means "the system is in location l_5 "
 - " $(\underline{l_i} = \overline{l_j})$ " stands for " $\bigwedge_n (\underline{l_i}[n] \leftrightarrow l_j[n])$ ",
 - "primed" variables l_i to represent location after transition
- Events: for each event $a \in \Sigma$, a Boolean variable <u>a</u>
 - a holds iff the system executes a switch with event a.
- Switches: for each switch $\langle I_i, a, \varphi, \lambda, I_i \rangle \in E$, a Boolean variable T,
 - T holds iff the system executes the corresponding switch
- Time elapse and null transitions: two variables T_{δ} and T_{null}^{j}
 - T_{δ} holds iff time elapses by some $\delta > 0$
 - T_{null}^{j} holds if and only A_{j} does nothing (specific for automaton A_{j})

- Locations: an array \underline{I} of $n \stackrel{\text{def}}{=} \lceil log_2(|L|) \rceil$ Boolean variables
 - I_i holds iff the system is in the location I_i
 - ex: " $\neg \underline{l_i}[3] \land \underline{l_i}[2] \land \neg \underline{l_i}[1] \land \underline{l_i}[0]$ " means "the system is in location $\underline{l_5}$ "
 - " $(\underline{l_i} = \overline{l_j})$ " stands for " $\bigwedge_n (\underline{l_i}[n] \leftrightarrow l_j[n])$ ",
 - "primed" variables $\underline{l_i}'$ to represent location after transition
- Events: for each event $a \in \Sigma$, a Boolean variable <u>a</u>
 - <u>a</u> holds iff the system executes a switch with event a.
- Switches: for each switch $\langle I_i, a, \varphi, \lambda, I_i \rangle \in E$, a Boolean variable T,
 - T holds iff the system executes the corresponding switch
- ullet Time elapse and null transitions: two variables T_δ and T^j_{null}
 - T_{δ} holds iff time elapses by some $\delta > 0$
 - T_{null}^{j} holds if and only A_{j} does nothing (specific for automaton A_{j})

- Locations: an array \underline{I} of $n \stackrel{\text{def}}{=} \lceil log_2(|L|) \rceil$ Boolean variables
 - I_i holds iff the system is in the location I_i
 - ex: " $\neg l_i[3] \wedge l_i[2] \wedge \neg l_i[1] \wedge l_i[0]$ " means "the system is in location l_5 "
 - " $(\underline{l_i} = \overline{l_j})$ " stands for " $\bigwedge_n (\underline{l_i}[n] \leftrightarrow l_j[n])$ ",
 - ullet "primed" variables $\underline{l_i}'$ to represent location after transition
- Events: for each event $a \in \Sigma$, a Boolean variable <u>a</u>
 - <u>a</u> holds iff the system executes a switch with event a.
- Switches: for each switch $\langle I_i, a, \varphi, \lambda, I_j \rangle \in E$, a Boolean variable T,
 - T holds iff the system executes the corresponding switch
- ullet Time elapse and null transitions: two variables T_δ and T^j_{null}
 - T_{δ} holds iff time elapses by some $\delta > 0$
 - T_{null}^{j} holds if and only A_{j} does nothing (specific for automaton A_{j})

- Locations: an array \underline{I} of $n \stackrel{\text{def}}{=} \lceil log_2(|L|) \rceil$ Boolean variables
 - I_i holds iff the system is in the location I_i
 - ex: " $\neg \underline{l_i}[3] \land \underline{l_i}[2] \land \neg \underline{l_i}[1] \land \underline{l_i}[0]$ " means "the system is in location $\underline{l_5}$ "
 - " $(\underline{l_i} = \overline{l_j})$ " stands for " $\bigwedge_n (\underline{l_i}[n] \leftrightarrow l_j[n])$ ",
 - ullet "primed" variables $\underline{l_i}'$ to represent location after transition
- Events: for each event $a \in \Sigma$, a Boolean variable <u>a</u>
 - <u>a</u> holds iff the system executes a switch with event a.
- Switches: for each switch $\langle I_i, a, \varphi, \lambda, I_i \rangle \in E$, a Boolean variable T,
 - T holds iff the system executes the corresponding switch
- ullet Time elapse and null transitions: two variables T_δ and T^j_{null}
 - T_{δ} holds iff time elapses by some $\delta > 0$
 - T_{null}^{j} holds if and only A_{j} does nothing (specific for automaton A_{j})

- Clocks values x are "normalized" wrt absolute time (t x):
 - a clock value x is written as difference t x
 - t represents the absolute time
 - "offset" variable x represents the absolute time when the clock was reset last time
- Clock constraints reduce to $(t x \bowtie c)$, $\bowtie \in \{\le, \ge, <, >\}$, $c \in \mathbb{Z}$
- Clock reset conditions reduce to (x := t)
- Clock equalities like $(x_k = x_l)$ reduce to $(t_k x_k = t_l x_l)$
 - appear only in loops
 - only place where full \mathcal{LRA} is needed (rather than \mathcal{DL})
 - \implies for invariant checking (no loops) \mathcal{DL} suffices
- Encoding the effect of transitions:
 - with a time-elapse transition:
 - t' > t, and x' = x
 - otherwise:

- Clocks values x are "normalized" wrt absolute time (t x):
 - a clock value x is written as difference t x
 - t represents the absolute time
 - "offset" variable x represents the absolute time when the clock was reset last time
- Clock constraints reduce to $(t x \bowtie c)$, $\bowtie \in \{\le, \ge, <, >\}$, $c \in \mathbb{Z}$
- Clock reset conditions reduce to (x := t)
- Clock equalities like $(x_k = x_l)$ reduce to $(t_k x_k = t_l x_l)$
 - appear only in loops
 - only place where full \mathcal{LRA} is needed (rather than \mathcal{DL})
 - \implies for invariant checking (no loops) \mathcal{DL} suffices
- Encoding the effect of transitions:
 - with a time-elapse transition:
 - \bullet t' > t, and x' = x
 - otherwise:

- Clocks values x are "normalized" wrt absolute time (t x):
 - a clock value x is written as difference t x
 - t represents the absolute time
 - "offset" variable x represents the absolute time when the clock was reset last time
- Clock constraints reduce to $(t x \bowtie c)$, $\bowtie \in \{\le, \ge, <, >\}$, $c \in \mathbb{Z}$
- Clock reset conditions reduce to (x := t)
- Clock equalities like $(x_k = x_l)$ reduce to $(t_k x_k = t_l x_l)$
 - appear only in loops
 - only place where full \mathcal{LRA} is needed (rather than \mathcal{DL})
 - \implies for invariant checking (no loops) \mathcal{DL} suffices
- Encoding the effect of transitions:
 - with a time-elapse transition:
 - \bullet t' > t, and x' = x
 - otherwise:

- Clocks values x are "normalized" wrt absolute time (t x):
 - a clock value x is written as difference t x
 - t represents the absolute time
 - "offset" variable x represents the absolute time when the clock was reset last time
- Clock constraints reduce to $(t x \bowtie c)$, $\bowtie \in \{\le, \ge, <, >\}$, $c \in \mathbb{Z}$
- Clock reset conditions reduce to (x := t)
- Clock equalities like $(x_k = x_l)$ reduce to $(t_k x_k = t_l x_l)$
 - appear only in loops
 - only place where full \mathcal{LRA} is needed (rather than \mathcal{DL})
 - \implies for invariant checking (no loops) \mathcal{DL} suffices
- Encoding the effect of transitions:
- with a time-elapse transition:
 - a t' > t and v' = v
 - a othonulas:

- Clocks values x are "normalized" wrt absolute time (t x):
 - a clock value x is written as difference t x
 - t represents the absolute time
 - "offset" variable x represents the absolute time when the clock was reset last time
- Clock constraints reduce to $(t x \bowtie c)$, $\bowtie \in \{\le, \ge, <, >\}$, $c \in \mathbb{Z}$
- Clock reset conditions reduce to (x := t)
- Clock equalities like $(x_k = x_l)$ reduce to $(t_k x_k = t_l x_l)$
 - appear only in loops
 - only place where full \mathcal{LRA} is needed (rather than \mathcal{DL})
 - \implies for invariant checking (no loops) \mathcal{DL} suffices
- Encoding the effect of transitions:
 - with a time-elapse transition:
 - t' > t, and x' = x
 - otherwise:
 - \bullet t'=t, absolute time does not elapse
 - x' = t', if the clock is reset
 - x' = x, if the clock is not reset

- Clocks values x are "normalized" wrt absolute time (t x):
 - a clock value x is written as difference t x
 - t represents the absolute time
 - "offset" variable x represents the absolute time when the clock was reset last time
- Clock constraints reduce to $(t x \bowtie c)$, $\bowtie \in \{\le, \ge, <, >\}$, $c \in \mathbb{Z}$
- Clock reset conditions reduce to (x := t)
- Clock equalities like $(x_k = x_l)$ reduce to $(t_k x_k = t_l x_l)$
 - appear only in loops
 - only place where full \mathcal{LRA} is needed (rather than \mathcal{DL})
 - \implies for invariant checking (no loops) \mathcal{DL} suffices
- Encoding the effect of transitions:
 - with a time-elapse transition:
 - t' > t, and x' = x
 - otherwise:

• t'=t, absolute time does not elapse

- x' = t', if the clock is reset
- x' = x, if the clock is not reset

- Clocks values x are "normalized" wrt absolute time (t x):
 - a clock value x is written as difference t x
 - t represents the absolute time
 - "offset" variable x represents the absolute time when the clock was reset last time
- Clock constraints reduce to $(t x \bowtie c)$, $\bowtie \in \{\le, \ge, <, >\}$, $c \in \mathbb{Z}$
- Clock reset conditions reduce to (x := t)
- Clock equalities like $(x_k = x_l)$ reduce to $(t_k x_k = t_l x_l)$
 - appear only in loops
 - only place where full \mathcal{LRA} is needed (rather than \mathcal{DL})
 - \implies for invariant checking (no loops) \mathcal{DL} suffices
- Encoding the effect of transitions:
 - with a time-elapse transition:
 - t' > t, and x' = x
 - otherwise:
 - t' = t, absolute time does not elapse
 - x' = t', if the clock is reset
 - x' = x, if the clock is not reset

- Clocks values x are "normalized" wrt absolute time (t x):
 - a clock value x is written as difference t x
 - t represents the absolute time
 - "offset" variable x represents the absolute time when the clock was reset last time
- Clock constraints reduce to $(t x \bowtie c)$, $\bowtie \in \{\le, \ge, <, >\}$, $c \in \mathbb{Z}$
- Clock reset conditions reduce to (x := t)
- Clock equalities like $(x_k = x_l)$ reduce to $(t_k x_k = t_l x_l)$
 - appear only in loops
 - only place where full \mathcal{LRA} is needed (rather than \mathcal{DL})
 - \implies for invariant checking (no loops) \mathcal{DL} suffices
- Encoding the effect of transitions:
 - with a time-elapse transition:
 - t' > t, and x' = x
 - otherwise:
 - t' = t, absolute time does not elapse
 - x' = t', if the clock is reset
 - x' = x, if the clock is not reset

- Clocks values x are "normalized" wrt absolute time (t x):
 - a clock value x is written as difference t x
 - t represents the absolute time
 - "offset" variable x represents the absolute time when the clock was reset last time
- Clock constraints reduce to $(t x \bowtie c)$, $\bowtie \in \{\le, \ge, <, >\}$, $c \in \mathbb{Z}$
- Clock reset conditions reduce to (x := t)
- Clock equalities like $(x_k = x_l)$ reduce to $(t_k x_k = t_l x_l)$
 - appear only in loops
 - only place where full \mathcal{LRA} is needed (rather than \mathcal{DL})
 - \implies for invariant checking (no loops) \mathcal{DL} suffices
- Encoding the effect of transitions:
 - with a time-elapse transition:
 - t' > t, and x' = x
 - otherwise:
 - t' = t, absolute time does not elapse
 - x' = t', if the clock is reset
 - x' = x, if the clock is not reset

Initial condition I(s):

Initially, the automaton is in an initial location:

$$\bigvee_{l_i \in L^0} \underline{l_i}$$

Initially, clocks have a null value:

$$\bigwedge_{x \in X} (x = t)$$

Remark

- in particular when encoding symbolically the discrete part of the system
- ullet e.g., there is probably a much more compact formula equivalent to $\bigvee_{l_i \in L^0} \underline{l_i}$



Initial condition I(s):

• Initially, the automaton is in an initial location:

$$\bigvee_{I_i \in L^0} \underline{I_i}$$

Initially, clocks have a null value:

$$\bigwedge_{x \in X} (x = t)$$

Remark

- in particular when encoding symbolically the discrete part of the system
- ullet e.g., there is probably a much more compact formula equivalent to $\bigvee_{l\in L^0} I_l$

Initial condition I(s):

• Initially, the automaton is in an initial location:

$$\bigvee_{I_i \in L^0} \underline{I_i}$$

Initially, clocks have a null value:

$$\bigwedge_{x\in X}(x=t)$$

Remark

- in particular when encoding symbolically the discrete part of the system
- ullet e.g., there is probably a much more compact formula equivalent to $\bigvee_{l\in L^0} I_l$

Initial condition I(s):

• Initially, the automaton is in an initial location:

$$\bigvee_{I_i \in L^0} \underline{I_i}$$

Initially, clocks have a null value:

$$\bigwedge_{x\in X}(x=t)$$

Remark

- in particular when encoding symbolically the discrete part of the system
- e.g., there is probably a much more compact formula equivalent to $\bigvee_{i \in L^0} \underline{I_i}$

Encoding: Invariants

Transition relation R(s, s'): Invariants

• Always, being in a location implies the corresponding invariant constraints:

$$\bigwedge_{l_i \in L} (\underline{l_i} \to \bigwedge_{\psi \in I(l_i)} \psi),$$

Transition relation T(s, s'):

Switches:

$$\bigwedge_{T \stackrel{\mathsf{def}}{=} \langle l_i, a, \varphi, \lambda, l_j \rangle \in E} \mathcal{T} \rightarrow \underbrace{\left(\underline{l_i} \land \underline{a} \land \varphi \land \underline{l_j'} \land (t' = t) \land \bigwedge_{x \in \lambda} (x' = t') \land \bigwedge_{x \not\in \lambda} (x' = x)\right)}_{X \not\in \lambda}$$

• Time elapse:

$$T_{\delta} \to \left((\underline{l'} = \underline{l}) \land (t' - t > 0) \land \bigwedge_{x \in X} (x' = x) \land \bigwedge_{a \in \Sigma} \neg \underline{a} \right)$$

• Null transition:

$$\Gamma^{j}_{null}
ightarrow \left((\underline{l'} = \underline{l}) \wedge (t' = t) \wedge \bigwedge_{x \in X} (x' = x) \wedge \bigwedge_{a \in \Sigma} \neg \underline{a} \right)$$

Transition relation T(s, s'):

Switches:

$$\bigwedge_{T \stackrel{\mathsf{def}}{=} \langle l_i, a, \varphi, \lambda, l_j \rangle \in E} T \rightarrow \left(\underline{l_i} \wedge \underline{a} \wedge \varphi \wedge \underline{l_j'} \wedge (t' = t) \wedge \bigwedge_{x \in \lambda} (x' = t') \wedge \bigwedge_{x \not\in \lambda} (x' = x) \right)$$

• Time elapse:

$$T_{\delta} \to \left((\underline{I'} = \underline{I}) \land (t' - t > 0) \land \bigwedge_{x \in X} (x' = x) \land \bigwedge_{a \in \Sigma} \neg \underline{a} \right)$$

Null transition:

$$T^{j}_{null} o \left((\underline{l'} = \underline{l}) \wedge (t' = t) \wedge \bigwedge_{x \in X} (x' = x) \wedge \bigwedge_{a \in \Sigma} \neg \underline{a} \right)$$

Transition relation T(s, s'):

Switches:

$$\bigwedge_{T \stackrel{\mathsf{def}}{=} \langle l_j, a, \varphi, \lambda, l_j \rangle \in \mathcal{E}} T \rightarrow \left(\underline{l_j} \wedge \underline{a} \wedge \varphi \wedge \underline{l_j'} \wedge (t' = t) \wedge \bigwedge_{x \in \lambda} (x' = t') \wedge \bigwedge_{x \not\in \lambda} (x' = x) \right)$$

• Time elapse:

$$T_{\delta} \rightarrow \left((\underline{l'} = \underline{l}) \wedge (t' - t > 0) \wedge \bigwedge_{x \in X} (x' = x) \wedge \bigwedge_{a \in \Sigma} \neg \underline{a} \right)$$

• Null transition:

$$T^{j}_{null}
ightarrow \left((\underline{l'} = \underline{l}) \wedge (t' = t) \wedge \bigwedge_{x \in X} (x' = x) \wedge \bigwedge_{a \in \Sigma} \neg \underline{a}
ight)$$

Transition relation T(s, s'):

Switches:

$$\bigwedge_{T \stackrel{\mathsf{def}}{=} \langle l_j, a, \varphi, \lambda, l_j \rangle \in \mathcal{E}} T \rightarrow \left(\underline{l_j} \wedge \underline{a} \wedge \varphi \wedge \underline{l_j'} \wedge (t' = t) \wedge \bigwedge_{x \in \lambda} (x' = t') \wedge \bigwedge_{x \not\in \lambda} (x' = x) \right)$$

• Time elapse:

$$T_{\delta} \rightarrow \left((\underline{l'} = \underline{l}) \wedge (t' - t > 0) \wedge \bigwedge_{x \in X} (x' = x) \wedge \bigwedge_{a \in \Sigma} \neg \underline{a} \right)$$

• Null transition:

$$T^{j}_{null} o \left((\underline{l'} = \underline{l}) \wedge (t' = t) \wedge \bigwedge_{x \in X} (x' = x) \wedge \bigwedge_{a \in \Sigma} \neg \underline{a} \right)$$

Mutual exclusion between events:

$$\bigwedge_{a_k,a_r\in\Sigma,a_k\neq a_r}(\neg\underline{a}_k\vee\neg\underline{a}_r)$$

• At least one transition takes place:

$$T_{null}^{j} \vee T_{\delta} \vee \bigvee_{T \in E} T$$

• Mutual exclusion between transitions:

$$\bigwedge_{T_k, T_r \in E \cup \{T_{out}^j\} \cup \{T_\delta\}, T_k \neq T_r} (\neg T_k \vee \neg T_r)$$

• Mutual exclusion between events:

$$\bigwedge_{a_k,a_r\in\Sigma,a_k\neq a_r} (\neg\underline{a}_k\vee\neg\underline{a}_r)$$

At least one transition takes place:

$$T_{null}^{j} \vee T_{\delta} \vee \bigvee_{T \in E} T$$

• Mutual exclusion between transitions:

$$\bigwedge_{T_k, T_r \in E \cup \{T_{out}^j\} \cup \{T_\delta\}, T_k \neq T_r} (\neg T_k \lor \neg T_r)$$

• Mutual exclusion between events:

$$\bigwedge_{a_k,a_r\in\Sigma,a_k\neq a_r} (\neg\underline{a}_k\vee\neg\underline{a}_r)$$

At least one transition takes place:

$$T_{null}^{j} \lor T_{\delta} \lor \bigvee_{T \in E} T$$

Mutual exclusion between transitions:

$$\bigwedge_{T_k, T_r \in E \cup \{T_{s,n}^j\} \cup \{T_k\}, T_k \neq T_r} (\neg T_k \lor \neg T_r)$$

• Mutual exclusion between events:

$$\bigwedge_{a_k,a_r\in\Sigma,a_k\neq a_r}(\neg\underline{a}_k\vee\neg\underline{a}_r)$$

At least one transition takes place:

$$T_{null}^{j} \lor T_{\delta} \lor \bigvee_{T \in E} T$$

Mutual exclusion between transitions:

$$\bigwedge_{T_k, T_r \in E \cup \{T_{null}^j\} \cup \{T_\delta\}, T_k \neq T_r} (\neg T_k \vee \neg T_r)$$

• Mutual exclusion between events:

$$\bigwedge_{a_k,a_r\in\Sigma,a_k\neq a_r}(\neg\underline{a}_k\vee\neg\underline{a}_r)$$

At least one transition takes place:

$$T_{null}^{j} \lor T_{\delta} \lor \bigvee_{T \in \mathcal{E}} T$$

Mutual exclusion between transitions:

$$\bigwedge_{T_k, T_r \in E \cup \{T_{null}^j\} \cup \{T_\delta\}, T_k \neq T_r} (\neg T_k \lor \neg T_r)$$

- The encoding is compositional wrt. product of automata
- The encoding of $A = A_1 || A_2$ is given by the conjunction of the encodings of A_1 and A_2 , plus a few extra axioms
- Mutual exclusion between events that are local

$$\bigwedge_{\substack{a_1 \in \Sigma_1 \setminus \Sigma_2 \\ a_2 \in \Sigma_2 \setminus \Sigma_1}} (\neg \underline{a_1} \vee \neg \underline{a_2})$$

$$\bigvee_{i=0}^{N-1} \neg T_{null}^{j}$$

- one distinct T_{null}^{j} for each automaton A_{j}
- T_{δ} is common to all automata A_i

- The encoding is compositional wrt. product of automata
- The encoding of $A = A_1 || A_2$ is given by the conjunction of the encodings of A_1 and A_2 , plus a few extra axioms
- Mutual exclusion between events that are local

$$igwedge_{a_1 \in \Sigma_1 \setminus \Sigma_2} (\neg \underline{a}_1 \lor \neg \underline{a}_2) \ a_2 \in \Sigma_2 \setminus \Sigma_1$$

$$\bigvee_{i=0}^{N-1} \neg T_{null}^{j}$$

- one distinct T_{null}^{j} for each automaton A_{j}
- T_{δ} is common to all automata A_i

- The encoding is compositional wrt. product of automata
- The encoding of $A = A_1 || A_2$ is given by the conjunction of the encodings of A_1 and A_2 , plus a few extra axioms
- Mutual exclusion between events that are local

$$\bigwedge_{\substack{a_1 \in \Sigma_1 \setminus \Sigma_2 \\ a_2 \in \Sigma_2 \setminus \Sigma_1}} (\neg \underline{a_1} \vee \neg \underline{a_2})$$

$$\bigvee_{i=0}^{N-1} \neg T_{null}^{j}$$

- one distinct T_{null}^{j} for each automaton A_{j}
- T_{δ} is common to all automata A_i

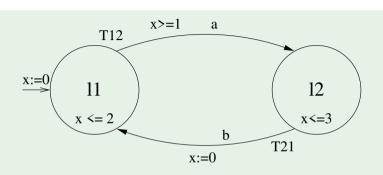
- The encoding is compositional wrt. product of automata
- The encoding of $A = A_1 || A_2$ is given by the conjunction of the encodings of A_1 and A_2 , plus a few extra axioms
- Mutual exclusion between events that are local

$$\bigwedge_{\substack{a_1 \in \Sigma_1 \setminus \Sigma_2 \\ a_2 \in \Sigma_2 \setminus \Sigma_1}} (\neg \underline{a}_1 \vee \neg \underline{a}_2)$$

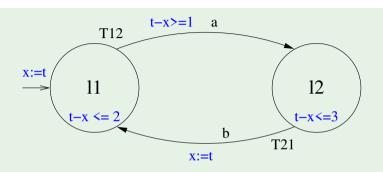
$$\bigvee_{i=0}^{N-1} \neg T_{null}^{j}$$

- one distinct T_{null}^{j} for each automaton A_{j}
- T_{δ} is common to all automata A_i

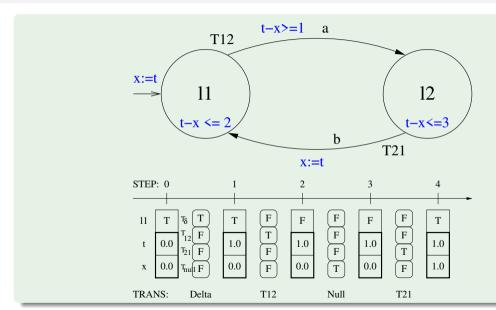
A Simple Example



A Simple Example



A Simple Example



147

Outline

- Motivations & Context
- Background (from previous chapters)
- SMT-Based Bounded Model Checking of Timed Systems
 - Basic Ideas
 - Basic Encoding
 - Improved & Extended Encoding
 - A Case-Study
- 4 SMT-Based Bounded Model Checking of Linear Hybrid Systems (hints)
- Proposed Exercises



Adding Global Variables

Dealing with some global variable v on discrete domain:

- A switch $T \stackrel{\text{def}}{=} \langle I_i, a, \varphi, \lambda, I_j \rangle$ can
 - be subject to a condition $\psi(v)$
 - \implies add $T \rightarrow \psi(v)$
 - assign v to some value n or keep its value

$$\implies$$
 add $T \rightarrow (v' = n)$ or add $T \rightarrow (v' = v)$

• T_{δ} mantains the value of v:

$$\implies$$
 add $T_{\delta} \rightarrow (v'=v)$

• T_{null}^{I} imposes no constraint on v:

```
\implies add nothing (for A_i)
```

Adding Global Variables

Dealing with some global variable v on discrete domain:

- A switch $T \stackrel{\text{def}}{=} \langle I_i, a, \varphi, \lambda, I_j \rangle$ can
 - be subject to a condition $\psi(v)$
 - \implies add $T \rightarrow \psi(v)$
 - assign v to some value n or keep its value

$$\implies$$
 add $T \rightarrow (v' = n)$ or add $T \rightarrow (v' = v)$

• T_{δ} mantains the value of v:

$$\implies$$
 add $T_\delta \rightarrow (V' = V)$

• T_{null}^{j} imposes no constraint on v:

 \Longrightarrow add nothing (for A_j)

Adding Global Variables

- A switch $T \stackrel{\text{def}}{=} \langle I_i, a, \varphi, \lambda, I_j \rangle$ can
 - be subject to a condition $\psi(v)$
 - \implies add $T \rightarrow \psi(v)$
 - assign v to some value n or keep its value

$$\implies$$
 add $T \rightarrow (v' = n)$ or add $T \rightarrow (v' = v)$

- T_{δ} mantains the value of v:
 - \implies add $T_{\delta} \rightarrow (v'=v)$
- T_{null}^{J} imposes no constraint on v:
 - \implies add nothing (for A_i)

Adding Global Variables

- A switch $T \stackrel{\text{def}}{=} \langle I_i, a, \varphi, \lambda, I_j \rangle$ can
 - be subject to a condition $\psi(v)$
 - \implies add $T \rightarrow \psi(v)$
 - assign v to some value n or keep its value

$$\implies$$
 add $T \rightarrow (v' = n)$ or add $T \rightarrow (v' = v)$

- T_{δ} mantains the value of v:
 - \implies add $T_8 \rightarrow (v'=v)$
- T_{null}^{j} imposes no constraint on v:
 - \implies add nothing (for A_i)

Adding Global Variables

- A switch $T \stackrel{\text{def}}{=} \langle I_i, a, \varphi, \lambda, I_j \rangle$ can
 - be subject to a condition $\psi(v)$
 - \implies add $T \rightarrow \psi(v)$
 - assign v to some value n or keep its value
 - \implies add $T \rightarrow (v' = n)$ or add $T \rightarrow (v' = v)$
- T_{δ} mantains the value of v:
- \implies add $T_{\delta} \rightarrow (v' = v)$
- T_{null}^{j} imposes no constraint on v:
 - \Rightarrow add nothing (for A_i)

Adding Global Variables

- A switch $T \stackrel{\text{def}}{=} \langle I_i, a, \varphi, \lambda, I_i \rangle$ can
 - be subject to a condition $\psi(v)$
 - \implies add $T \rightarrow \psi(v)$
 - assign v to some value n or keep its value
 - \implies add $T \rightarrow (v' = n)$ or add $T \rightarrow (v' = v)$
- T_{δ} mantains the value of v:
 - \implies add $T_{\delta} \rightarrow (v' = v)$
- T_{null}^{j} imposes no constraint on v:
 - \Rightarrow add nothing (for A_i)

Adding Global Variables

- A switch $T \stackrel{\text{def}}{=} \langle I_i, a, \varphi, \lambda, I_j \rangle$ can
 - be subject to a condition $\psi(v)$
 - \implies add $T \rightarrow \psi(v)$
 - assign v to some value n or keep its value
 - \implies add $T \rightarrow (v' = n)$ or add $T \rightarrow (v' = v)$
- T_{δ} mantains the value of v:
 - \implies add $T_{\delta} \rightarrow (v' = v)$
- T_{null}^{j} imposes no constraint on v:
 - \Rightarrow add nothing (for A_i)

Adding Global Variables

- A switch $T \stackrel{\text{def}}{=} \langle I_i, a, \varphi, \lambda, I_j \rangle$ can
 - be subject to a condition $\psi(v)$
 - \implies add $T \rightarrow \psi(v)$
 - assign v to some value n or keep its value
 - \implies add $T \rightarrow (v' = n)$ or add $T \rightarrow (v' = v)$
- T_{δ} mantains the value of v:
 - \implies add $T_{\delta} \rightarrow (v' = v)$
- T_{null}^{j} imposes no constraint on v:
 - \implies add nothing (for A_i)

Adding Global Variables

- A switch $T \stackrel{\text{def}}{=} \langle I_i, a, \varphi, \lambda, I_j \rangle$ can
 - be subject to a condition $\psi(v)$
 - \implies add $T \rightarrow \psi(v)$
 - assign v to some value n or keep its value
 - \implies add $T \rightarrow (v' = n)$ or add $T \rightarrow (v' = v)$
- T_{δ} mantains the value of v:
 - \implies add $T_{\delta} \rightarrow (v' = v)$
- T_{null}^{j} imposes no constraint on v:
 - \implies add nothing (for A_j)

MATHSAT: Optimizations

Customization of MATHSAT

• Limit Boolean variable-selection heuristic to pick transition variables, in forward order

Encoding: Optimizations

Boolean Propagation of Math Constraints:

Idea: add small and mathematically-obvious lemmas

- ⇒ force assignments by unit-propagation,
- \Longrightarrow saves calls to the \mathcal{T} -Solvers

Encoding Variants

Shortening counter-examples:

- Collapsing consequent time elapsing transitions:
 - $s \stackrel{\delta}{\longmapsto} s, s \stackrel{\delta'}{\longmapsto} s$ reduced to $s \stackrel{\delta+\delta'}{\longmapsto} s$
 - add $\neg T_{\delta} \lor \neg T'_{\delta}$ to transition relation R(s, s')
 - ⇒ implements the notion of "non-Zeno-ness" (see previous chapter)
- Allow multiple parallel transitions
 - remove mutex between labels local to processes
 - allows a form of parallel progression

Remark: may change the notion of "next step" ⇒ only if no "X" operators occurs in property!

Encoding Variants

Shortening counter-examples:

- Collapsing consequent time elapsing transitions:
 - $s \stackrel{\delta}{\longmapsto} s, s \stackrel{\delta'}{\longmapsto} s$ reduced to $s \stackrel{\delta+\delta'}{\longmapsto} s$
 - add $\neg T_{\delta} \lor \neg T'_{\delta}$ to transition relation R(s, s')
 - ⇒ implements the notion of "non-Zeno-ness" (see previous chapter)
- Allow multiple parallel transitions
 - remove mutex between labels local to processes
 - ⇒ allows a form of parallel progression

Remark: may change the notion of "next step" ⇒ only if no "X" operators occurs in property!

Encoding Variants

Shortening counter-examples:

- Collapsing consequent time elapsing transitions:
 - $s \stackrel{\delta}{\longmapsto} s$, $s \stackrel{\delta'}{\longmapsto} s$ reduced to $s \stackrel{\delta+\delta'}{\longmapsto} s$
 - add $\neg T_{\delta} \lor \neg T'_{\delta}$ to transition relation R(s, s')
 - ⇒ implements the notion of "non-Zeno-ness" (see previous chapter)
- Allow multiple parallel transitions
 - remove mutex between labels local to processes
 - ⇒ allows a form of parallel progression

Remark: may change the notion of "next step" ⇒ only if no "X" operators occurs in property!

Encoding Variants (cont.)

A limited form of symmetry reduction

If N automata are symmetric (frequent with protocol verification):

- Intuition: restrict executions s.t.
 - At step 0 only A₀ can move
 - At step 1 only A₀, A₁ can move
 - At step 2 only A_0, A_1, A_2 can move
 - ...
 - ⇒ we name "0" the first automata who acts, "1" the second one, etc.
- for step i < N-1, we drop the disjunct $\neg T_{null}^{i+1}$ $(i) \lor \ldots \lor \neg T_{null}^{N-1}$:

set
$$\bigvee_{j=0}^{min(i,N-1)} \neg T_{null}^{j\ (i)}$$
 rather than $\bigvee_{j=0}^{N-1} \neg T_{null}^{j\ (i)}$

- \implies drops "symmetric" executions
- \implies reduces the search space of a up to $2^{N(N-1)/2}$ factor!

Encoding Variants (cont.)

A limited form of symmetry reduction

If N automata are symmetric (frequent with protocol verification):

- Intuition: restrict executions s.t.
 - At step 0 only A_0 can move
 - At step 1 only A₀, A₁ can move
 - At step 2 only A_0, A_1, A_2 can move
 - ...
 - ⇒ we name "0" the first automata who acts, "1" the second one, etc.
- for step i < N-1, we drop the disjunct $\neg T_{null}^{i+1}$ $\forall \ldots \lor \neg T_{null}^{N-1}$:

set
$$\bigvee_{j=0}^{\min(i,N-1)} \neg T_{null}^{j\ (i)}$$
 rather than $\bigvee_{j=0}^{N-1} \neg T_{null}^{j\ (i)}$

- \implies drops "symmetric" executions
- \implies reduces the search space of a up to $2^{N(N-1)/2}$ factor!

Encoding Variants (cont.)

A limited form of symmetry reduction

If N automata are symmetric (frequent with protocol verification):

- Intuition: restrict executions s.t.
 - At step 0 only A₀ can move
 - At step 1 only A₀, A₁ can move
 - At step 2 only A_0, A_1, A_2 can move
 - ...
 - ⇒ we name "0" the first automata who acts, "1" the second one, etc.
- for step i < N-1, we drop the disjunct $\neg T_{null}^{i+1}(i) \lor \ldots \lor \neg T_{null}^{N-1}(i)$:

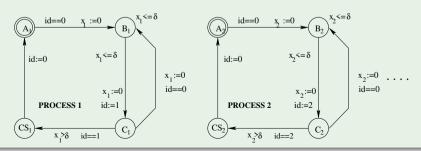
$$set \bigvee_{j=0}^{\min(i,N-1)} \neg T_{null}^{j\ (i)} \quad rather\ than \bigvee_{j=0}^{N-1} \neg T_{null}^{j\ (i)}$$

- drops "symmetric" executions
- \implies reduces the search space of a up to $2^{N(N-1)/2}$ factor!

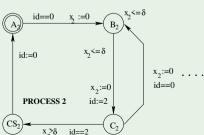
Outline

- Motivations & Context
- Background (from previous chapters
- SMT-Based Bounded Model Checking of Timed Systems
 - Basic Ideas
 - Basic Encoding
 - Improved & Extended Encoding
 - A Case-Study
- 4 SMT-Based Bounded Model Checking of Linear Hybrid Systems (hints)
- Proposed Exercises

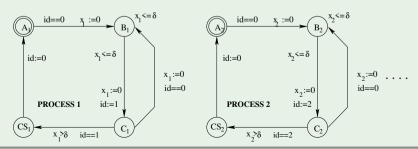
- N identical processes accessing one critical section
- shared variable $id \in \{0, 1, 2, ..., N\}$: process identifier (0: none)
 - when entering wait state C_i , agent A_i writes its code on id
 - if id = j after δ , then A_i can enter the critical session
- Two properties under test
 - Reachability: EF ∧, P_i.C (reached in N+1 steps)
 - Fairness: $\mathbf{E} = (\mathbf{GF}P_1.B \rightarrow \mathbf{GF}P_1.CS)$ (reached in N+5 steps)



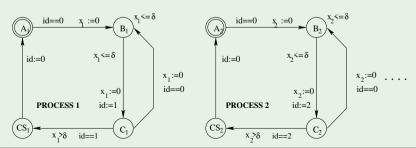
- N identical processes accessing one critical section
- shared variable $id \in \{0, 1, 2, ..., N\}$: process identifier (0: none)
 - when entering wait state C_j , agent A_j writes its code on id
 - if id = j after δ , then A_j can enter the critical session
- Two properties under test
 - Reachability: EF A, P_i, C (reached in N+1 steps)
 - Fairness: $E \neg (GFP_i.B \rightarrow GFP_i.CS)$ (reached in N+5 step



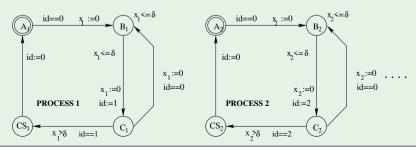
- N identical processes accessing one critical section
- shared variable $id \in \{0, 1, 2, ..., N\}$: process identifier (0: none)
 - when entering wait state C_j , agent A_j writes its code on id
 - if id = j after δ , then A_i can enter the critical session
- Two properties under test
 - Reachability: **EF** $\bigwedge_i P_i.C$ (reached in N+1 steps)
 - Fairness: $\mathbf{E} \neg (\mathbf{GF}P_i.B \rightarrow \mathbf{GF}P_i.CS)$ (reached in N+5 steps)



- N identical processes accessing one critical section
- shared variable $id \in \{0, 1, 2, ..., N\}$: process identifier (0: none)
 - when entering wait state C_i , agent A_i writes its code on id
 - if id = j after δ , then A_i can enter the critical session
- Two properties under test
 - Reachability: $\mathbf{EF} \bigwedge_{i} P_{i}.C$ (reached in N+1 steps)
 - Fairness: $\mathbf{E} \neg (\mathbf{GF}P_i.B \rightarrow \mathbf{GF}P_i.CS)$ (reached in N+5 steps)



- N identical processes accessing one critical section
- shared variable $id \in \{0, 1, 2, ..., N\}$: process identifier (0: none)
 - when entering wait state C_i , agent A_i writes its code on id
 - if id = j after δ , then A_i can enter the critical session
- Two properties under test
 - Reachability: $\mathbf{EF} \bigwedge_{i} P_{i}.C$ (reached in N+1 steps)
 - Fairness: $\mathbf{E} \neg (\mathbf{GF}P_i.B \rightarrow \mathbf{GF}P_i.CS)$ (reached in N+5 steps)



Fischer's protocol: (cont.)

Exercise:

- Why is $\mathbf{EF} \wedge_i P_i \cdot C$ reached in N+1 steps?
- Why is $\mathbf{E} \neg (\mathbf{GF}P_i.B \rightarrow \mathbf{GF}P_i.CS)$ reached in N+5 steps?

(See [Audemard et al, FORTE'02] for the solution.)

Fischer's protocol: (reachability)

$M \models_k \mathbf{EF} \bigwedge_i P_i.C$

	Матн	SATI	Матн	SAT,Sym	DE	D	UPF	PAL	Kroi	NOS	RE	D	RED,	Sym
N	Time	Size	Time	Size	Time	Size	Time	Size	Time	Size	Time	Size	Time	Size
3	0.05	2.9	0.04	2.9	0.11	106	0.01	1.7	0.01	0.8	0.23	2.0	0.19	2.0
4	0.09	3.0	0.08		0.14		0.02	1.9				2.1	0.70	2.1
5	0.20	3.2	0.16		0.24		0.21	1.9				2.2		2.4
6	0.60	3.7			0.47		3.44		0.39		12.00	2.7	5.20	3.1
7	3.20	4.2			1.30		153	54		MEM		4.0		
8	29	4.9	0.52		3.96		TIME				121	7.6		7.8
9	343	5.9		5.9				- 1			416	16.6		13.3
10		6.5	1.01	6.5							1382	39		23
	TIME		1.39	7.0		106					TIME		157	38
12		l	1.89	7.5		MEM							266	63
13			2.44	8.2									439	100
14			3.24	8.9									709	155
15			4.11	9.7									1118	225
16			5.10	10.7									1717	342
17			6.30	11.7									2582	492
18			8.00	12.9									TIME	
19			9.50	14.2										

(MATHSAT times are sum of all instances up to k)

Fischer's protocol (liveness violation)

$$M \models_{k} \mathbf{E} \neg (\mathbf{GF}P_{i}.B \rightarrow \mathbf{GF}P_{i}.CS)$$

			MATH:	SAT		MATHSAT with Boehm heuristic					
$k \setminus N$	2	3	4	5	6	2	3	4	5	6	
2	0.01	0.01	0.01	0.01	0.02	0.01	0.01	0.01	0.01	0.02	
3	0.01	0.02	0.01	0.01	0.03	0.01	0.01	0.02	0.03	0.04	
4	0.01	0.02	0.02	0.02	0.04	0.01	0.02	0.04	0.07	0.17	
5	0.02	0.03	0.05	0.09	0.18	0.01	0.03	0.09	0.30	1.16	
6	0.03	0.10	0.21	0.54	1.35	0.02	0.07	0.31	1.52	7.74	
7	0.04	0.26	0.97	3.20	9.83	0.02	0.18	1.19	7.14	45.00	
8		0.65	4.80	19.72	70.70		0.06	4.70	33.50	242.00	
9			5.55	112.17	478.00			0.61	165.90	1348.00	
10				303.17	3086.00				9.92	7824.00	
11					5002.00					252.00	
Σ	0.12	1.08	11.62	438.93	8648.15	0.07	0.37	6.98	218.40	9720.13	

Outline

- Motivations & Context
- Background (from previous chapters)
- SMT-Based Bounded Model Checking of Timed Systems
 - Basic Ideas
 - Basic Encoding
 - Improved & Extended Encoding
 - A Case-Study
- SMT-Based Bounded Model Checking of Linear Hybrid Systems (hints)
- Proposed Exercises



The encoding

Given a Linear hybrid automaton A and a LTL formula f:

• The encoding $[[A, f]]_k$ is obtained following the same schema as in propositional BMC:

$$[[A, f]]_k := I(s^{(0)}) \wedge \bigwedge_{i=0}^{k-1} R(s^{(i)}, s^{(i+1)}) \wedge (\neg L_k \wedge [[f]]_k^0) \vee \bigvee_{l=0}^k ({}_{l}L_k \wedge {}_{l}[[f]]_k^0)$$

- $[[M, f]]_k$ is a \mathcal{LRA} -formula, where
 - Boolean variables encode the discrete part of the state of the automaton
 - a real variable *t* (rational for rectangular automata) encodes absolute time elapse
 - real (rational) variables $x \in X$ encode continuous variables
 - constraints on real (rational) variables represent the continuous flow part of the state

The encoding

Given a Linear hybrid automaton A and a LTL formula f:

• The encoding $[[A, f]]_k$ is obtained following the same schema as in propositional BMC:

$$[[A, f]]_k := I(s^{(0)}) \wedge \bigwedge_{i=0}^{k-1} R(s^{(i)}, s^{(i+1)}) \wedge (\neg L_k \wedge [[f]]_k^0) \vee \bigvee_{l=0}^k ({}_{l}L_k \wedge {}_{l}[[f]]_k^0)$$

- $[[M, f]]_k$ is a \mathcal{LRA} -formula, where
 - Boolean variables encode the discrete part of the state of the automaton
 - a real variable t (rational for rectangular automata) encodes absolute time elapse
 - real (rational) variables $x \in X$ encode continuous variables
 - constraints on real (rational) variables represent the continuous flow part of the state

Encoding: Boolean Variables

- Locations: I, as with timed systems
- Events: $a \in \Sigma$, as with timed systems
- Switches: T, as with timed systems
- Time elapse and null transitions: T_{δ} and T_{null}^{j} , as with timed systems

- Continuous variables:
 - t represents the absolute time
 - real (rational) variables x represent continuous values
- Continuous constraints (initial, guards, invariants) reduce to linear constraints on X: $\sum_{x_i \in X} a_i x_i \bowtie c$ s.t. $\bowtie \in \{\leq, \geq, <, >\}, c \in \mathbb{Q}$
- Encoding the effect of discrete transitions:
 - ullet r=t, absolute time does not elapse ullet Jumo relations reduce to linear transformations: Λ \mathcal{M} :=
- Encoding the effect of time-elapse transitions:
 - t' > t• $\bigwedge_j \Psi_j(X, t, X', t) \ge 0$
 - where $\Psi_l(X,t,X',t) \stackrel{\text{def}}{=} \sum_i a_{ij}(x_i'-x_i) + c(t'-t) \geq 0$, given $\bigwedge_i \sum_i a_{ij} \frac{\partial x_i}{\partial t} + c \geq 0$

- Continuous variables:
 - t represents the absolute time
 - real (rational) variables x represent continuous values
- Continuous constraints (initial, guards, invariants) reduce to linear constraints on X:

$$\sum_{x_i \in X} a_i x_i \bowtie c \text{ s.t. } \bowtie \in \{\leq, \geq, <, >\}, c \in \mathbb{Q}$$

- $x_i \bowtie c_i$ with rectangular automata
- Encoding the effect of discrete transitions:
 - t'=t absolute time does not elapse
 - \bullet Jump relations reduce to linear transformations: $\bigwedge, \chi'_i := \sum_i a_{ij} x_i + \alpha_i x_i$
- Encoding the effect of time-elapse transitions:
 - $\bullet t' > t$
 - $\bullet \ \Lambda_i \Psi_i(X,t,X',t) > 0$
 - where $\Psi_l(X,t,X',t) \cong \sum_i a_i(x_i'-x_i) + c(t'-t) \geq 0$, given $\bigwedge_i \sum_i a_i \stackrel{\text{def}}{\longrightarrow} + c \geq 0$

- Continuous variables:
 - t represents the absolute time
 - real (rational) variables x represent continuous values
- Continuous constraints (initial, guards, invariants) reduce to linear constraints on X:

```
\sum_{x_i \in X} a_i x_i \bowtie c s.t. \bowtie \in \{\leq, \geq, <, >\}, c \in \mathbb{Q}
```

- $x_i \bowtie c_i$ with rectangular automata
- Encoding the effect of discrete transitions:

```
a Jump relations reduce to linear transformation
```

- ullet Jump relations reduce to linear transformations: $igwedge_i x_i' := \sum_i a_{ij}$
- Encoding the effect of time-elapse transitions:

$$\bullet \ \bigwedge_{i} \Psi_{i}(X, t, X', t) \geq 0$$

where $\Psi_i(X,t,X',t) \cong \sum_i a_{ij}(x_i'-x_j)+c(t'-t)\geq 0$, given $A_i\sum_i a_{ij} \stackrel{\text{def}}{=} +c\geq 0$

- Continuous variables:
 - t represents the absolute time
 - real (rational) variables x represent continuous values
- Continuous constraints (initial, guards, invariants) reduce to linear constraints on X:

```
\sum_{x_i \in X} a_i x_i \bowtie c s.t. \bowtie \in \{\leq, \geq, <, >\}, c \in \mathbb{Q}
```

- $x_i \bowtie c_i$ with rectangular automata
- Encoding the effect of discrete transitions:
 - t' = t, absolute time does not elapse
 - Jump relations reduce to linear transformations: $\bigwedge_j x_j' := \sum_i a_{ij} x_i + c_j$
- Encoding the effect of time-elapse transitions:
- $0 \wedge W(X + X' +) > 0$

 - where $\Psi_l(X,t,X',t) \cong \sum_i a_l(x_i'-x_i) + c(t'-t) \geq 0$, given $\bigwedge_i \sum_i a_l \stackrel{\text{\tiny def}}{=} + c \geq 0$

- Continuous variables:
 - t represents the absolute time
 - real (rational) variables x represent continuous values
- Continuous constraints (initial, guards, invariants) reduce to linear constraints on X:

```
\sum_{x_i \in X} a_i x_i \bowtie c \text{ s.t. } \bowtie \in \{\leq, \geq, <, >\}, c \in \mathbb{Q}
```

- $x_i \bowtie c_i$ with rectangular automata
- Encoding the effect of discrete transitions:
 - t' = t, absolute time does not elapse
 - Jump relations reduce to linear transformations: $\bigwedge_j x_j' := \sum_i a_{ij} x_i + c_j$
 - $\bigwedge_{x_i \in X} (x_i' := c_i)$ with rectangular automata
- Encoding the effect of time-elapse transitions:

$$\bullet t' > t$$

where $\Psi_i(X,t,X',t) \stackrel{\text{\tiny def}}{=} \sum_i a_{il}(x_i'-x_i) + c(t'-t) \geq 0$, given $\bigwedge_i \sum_i a_{il} \frac{dx_i}{dt} + c \geq 0$

- Continuous variables:
 - t represents the absolute time
 - real (rational) variables x represent continuous values
- Continuous constraints (initial, guards, invariants) reduce to linear constraints on X:

```
\sum_{x_i \in X} a_i x_i \bowtie c s.t. \bowtie \in \{\leq, \geq, <, >\}, c \in \mathbb{Q}
```

- $x_i \bowtie c_i$ with rectangular automata
- Encoding the effect of discrete transitions:
 - t' = t, absolute time does not elapse
 - Jump relations reduce to linear transformations: $\bigwedge_j x_j' := \sum_i a_{ij} x_i + c_j$
 - $\bigwedge_{x_i \in X} (x'_i := c_i)$ with rectangular automata
- Encoding the effect of time-elapse transitions:
 - $\bullet t' > t$

 - where $\Psi_l(X,t,X',t) \cong \sum_i a_i(x_i'-x_i) + c(t'-t) \geq 0$, given $\bigwedge_i \sum_i a_i \stackrel{\text{def}}{\longrightarrow} + c \geq 0$

- Continuous variables:
 - t represents the absolute time
 - real (rational) variables x represent continuous values
- Continuous constraints (initial, guards, invariants) reduce to linear constraints on X:

$$\sum_{x_i \in X} a_i x_i \bowtie c$$
 s.t. $\bowtie \in \{\leq, \geq, <, >\}, c \in \mathbb{Q}$

- $x_i \bowtie c_i$ with rectangular automata
- Encoding the effect of discrete transitions:
 - t' = t, absolute time does not elapse
 - Jump relations reduce to linear transformations: $\bigwedge_j x_j' := \sum_i a_{ij} x_i + c_j$
 - $\bigwedge_{x_i \in X} (x'_i := c_i)$ with rectangular automata
- Encoding the effect of time-elapse transitions:
 - \bullet t' > t
 - $\bigwedge_{j} \Psi_{j}(X, t, X', t) \geq 0$

where
$$\Psi_j(X,t,X',t) \stackrel{\text{def}}{=} \sum_i a_{ij}(x_i'-x_i) + c(t'-t) \ge 0$$
, given $\bigwedge_j \sum_i a_{ij} \frac{dx_i}{dt} + c \ge 0$

with rectangular automata:

 $(x_i'-x_i \leq c_i^M(t'-t)+b_i^M), (x_i'-x_i \geq c_i^m(t'-t)+b_i^m) \text{ s.t. } c_i^M \stackrel{\text{def}}{=} \max\{\frac{dx_i}{dt}\}, c_i^m \stackrel{\text{def}}{=} \min\{\frac{dx_i}{dt}\}$

- Continuous variables:
 - t represents the absolute time
 - real (rational) variables x represent continuous values
- Continuous constraints (initial, guards, invariants) reduce to linear constraints on X:

$$\sum_{x_i \in X} a_i x_i \bowtie c$$
 s.t. $\bowtie \in \{\leq, \geq, <, >\}, c \in \mathbb{Q}$

- $x_i \bowtie c_i$ with rectangular automata
- Encoding the effect of discrete transitions:
 - t' = t, absolute time does not elapse
 - Jump relations reduce to linear transformations: $\bigwedge_j x_j' := \sum_i a_{ij} x_i + c_j$
 - $\bigwedge_{x_i \in X} (x'_i := c_i)$ with rectangular automata
- Encoding the effect of time-elapse transitions:
 - \bullet t' > t
 - $\bigwedge_{j} \Psi_{j}(X, t, X', t) \geq 0$

where
$$\Psi_j(X, t, X', t) \stackrel{\text{def}}{=} \sum_i a_{ij} (x_i' - x_i) + c(t' - t) \ge 0$$
, given $\bigwedge_i \sum_i a_{ij} \frac{dx_i}{dt} + c \ge 0$

with rectangular automata:

$$(x_i' - x_i \le c_i^M(t' - t) + b_i^M), (x_i' - x_i \ge c_i^m(t' - t) + b_i^m) \text{ s.t. } c_i^M \stackrel{\text{def}}{=} max\{\frac{dx_i}{dt}\}, c_i^m \stackrel{\text{def}}{=} min\{\frac{dx_i}{dt}\}$$

- Continuous variables:
 - t represents the absolute time
 - real (rational) variables x represent continuous values
- Continuous constraints (initial, guards, invariants) reduce to linear constraints on X:

$$\sum_{x_i \in X} a_i x_i \bowtie c$$
 s.t. $\bowtie \in \{\leq, \geq, <, >\}, c \in \mathbb{Q}$

- $x_i \bowtie c_i$ with rectangular automata
- Encoding the effect of discrete transitions:
 - t' = t, absolute time does not elapse
 - Jump relations reduce to linear transformations: $\bigwedge_i x_j' := \sum_i a_{ij} x_i + c_j$
 - $\bigwedge_{x_i \in X} (x_i' := c_i)$ with rectangular automata
- Encoding the effect of time-elapse transitions:
 - \bullet t' > t
 - $\bigwedge_{j} \Psi_{j}(X, t, X', t) \geq 0$

where
$$\Psi_j(X, t, X', t) \stackrel{\text{def}}{=} \sum_i a_{ij} (x_i' - x_i) + c(t' - t) \ge 0$$
, given $\bigwedge_i \sum_i a_{ij} \frac{dx_i}{dt} + c \ge 0$

with rectangular automata:

$$(x_i' - x_i \leq c_i^M(t' - t) + b_i^M), (x_i' - x_i \geq c_i^m(t' - t) + b_i^m) \text{ s.t. } c_i^M \stackrel{\text{def}}{=} max\{\frac{dx_i}{dt}\}, c_i^m \stackrel{\text{def}}{=} min\{\frac{dx_i}{dt}\},$$

Initial condition I(s):

• Initially, the automaton is in an initial location:

$$t=0 o \bigvee_{I_i \in L^0} \underline{I_i}$$

Initially, clocks comply with initial conditions:

$$t = 0 \to \bigwedge_{l_i \in L^0} (\underline{l_i} \to \mathit{Init}_l(X))$$

Transition relation R(s, s'): Invariants

• Always, being in a location implies the corresponding invariant constraints:

$$\bigwedge_{l_i \in L} (\underline{l_i} \to \bigwedge_{\psi \in I(l_i)} \psi),$$

Initial condition I(s):

• Initially, the automaton is in an initial location:

$$t=0 o \bigvee_{I_i \in L^0} \underline{I_i}$$

Initially, clocks comply with initial conditions:

$$t=0
ightarrow igwedge_{l_i\in L^0}(\underline{l_i}
ightarrow \mathit{Init}_l(X))$$

Transition relation R(s, s'): Invariants

• Always, being in a location implies the corresponding invariant constraints:

$$\bigwedge_{l_i \in L} (\underline{l_i} \to \bigwedge_{\psi \in I(l_i)} \psi),$$



Initial condition I(s):

• Initially, the automaton is in an initial location:

$$t=0\to\bigvee_{I_i\in L^0}\underline{I_i}$$

• Initially, clocks comply with initial conditions:

$$t = 0 \to \bigwedge_{l_l \in L^0} (\underline{l_l} \to Init_l(X))$$

Transition relation R(s, s'): Invariants

Always, being in a location implies the corresponding invariant constraints:

$$\bigwedge_{l_i \in L} (\underline{l_i} \to \bigwedge_{\psi \in I(l_i)} \psi),$$



Initial condition I(s):

• Initially, the automaton is in an initial location:

$$t=0\to\bigvee_{I_i\in L^0}\underline{I_i}$$

• Initially, clocks comply with initial conditions:

$$t = 0 \to \bigwedge_{l_i \in L^0} (\underline{l_i} \to \mathit{Init}_l(X))$$

Transition relation R(s, s'): Invariants

• Always, being in a location implies the corresponding invariant constraints:

$$\bigwedge_{I_i \in L} (\underline{I_i} \to \bigwedge_{\psi \in I(I_i)} \psi),$$



Encoding (linear automata): Transitions

Transition relation T(s, s'):

Switches:

$$\bigwedge_{T \stackrel{\mathsf{def}}{=} \langle l_i, a, \varphi, J, l_j \rangle \in E} T \rightarrow \left(\underline{l_i} \land \underline{a} \land \varphi \land \underline{l_j'} \land (t' = t) \land \bigwedge_{x_j \in X} (x_j' := \sum_i a_{ij} x_i + c_j) \right)$$

• Time elapse:

$$T_{\delta} \to \left((\underline{l'} = \underline{l}) \wedge (t' - t > 0) \wedge (\bigwedge_{j} \Psi_{j}(X, t, X', t) \geq 0) \wedge \bigwedge_{a \in \Sigma} \neg \underline{a} \right)$$

• Null transition:

$$\frac{d}{dt} au_{null} o \left((\underline{l'} = \underline{l}) \wedge (t' = t) \wedge \bigwedge_{x_i \in X} (x'_i = x_i) \wedge \bigwedge_{a \in \Sigma} \neg \underline{a} \right)$$

Encoding (linear automata): Transitions

Transition relation T(s, s'):

Switches:

$$\bigwedge_{T \stackrel{\mathsf{def}}{=} \langle l_i, a, \varphi, J, l_j \rangle \in \mathcal{E}} T \rightarrow \left(\underline{l_i} \wedge \underline{a} \wedge \varphi \wedge \underline{l_j'} \wedge (t' = t) \wedge \bigwedge_{x_j \in X} (x_j' := \sum_i a_{ij} x_i + c_j) \right)$$

• Time elapse:

$$\mathcal{T}_{\delta}
ightarrow \left((\underline{l'} = \underline{l}) \wedge (t' - t > 0) \wedge (\bigwedge_{j} \Psi_{j}(X, t, X', t) \geq 0) \wedge \bigwedge_{a \in \Sigma} \neg \underline{a} \right)$$

• Null transition:

$$\stackrel{-j}{\operatorname{null}} o \left((\underline{l'} = \underline{l}) \wedge (t' = t) \wedge \bigwedge_{x_i \in X} (x'_i = x_i) \wedge \bigwedge_{a \in \Sigma} \neg \underline{a} \right)$$



Encoding (linear automata): Transitions

Transition relation T(s, s'):

Switches:

$$igwedge T o \left(\underbrace{I_{\underline{i}} \wedge \underline{a} \wedge arphi \wedge I_{\underline{j}'} \wedge (t'=t) \wedge igwedge _{x_j \in X} (x_j' := \sum_i a_{ij} x_i + c_j)
ight)$$

• Time elapse:

$$T_{\delta}
ightarrow \left((\underline{l'} = \underline{l}) \wedge (t' - t > 0) \wedge (\bigwedge_{j} \Psi_{j}(X, t, X', t) \geq 0) \wedge \bigwedge_{a \in \Sigma} \neg \underline{a} \right)$$

$$T_{null}^{j}
ightarrow \left((\underline{l}' = \underline{l}) \wedge (t' = t) \wedge \bigwedge_{x_i \in X} (x_i' = x_i) \wedge \bigwedge_{a \in \Sigma} \neg \underline{a} \right)$$

Encoding (linear automata): Transitions

Transition relation T(s, s'):

Switches:

$$igwedge T o \left(\underbrace{I_{\underline{i}} \wedge \underline{a} \wedge arphi \wedge I_{\underline{j}'} \wedge (t'=t) \wedge igwedge _{x_j \in X} (x_j' := \sum_i a_{ij} x_i + c_j)
ight)$$

• Time elapse:

$$T_{\delta}
ightarrow \left((\underline{l'} = \underline{l}) \wedge (t' - t > 0) \wedge (\bigwedge_{j} \Psi_{j}(X, t, X', t) \geq 0) \wedge \bigwedge_{a \in \Sigma} \neg \underline{a} \right)$$

$$T_{null}^{j} \rightarrow \left((\underline{l'} = \underline{l}) \wedge (t' = t) \wedge \bigwedge_{x_i \in X} (x'_i = x_i) \wedge \bigwedge_{a \in \Sigma} \neg \underline{a} \right)$$

Transition relation T(s, s'):

Switches:

$$igwedge_{T \stackrel{\mathsf{def}}{=} \langle l_i, a, arphi, \lambda, l_j
angle \in E} \left(\underline{l_i} \wedge \underline{a} \wedge arphi \wedge \underline{l_j'} \wedge (t' = t) \wedge igwedge_{x_i \in X} (x_i' := c_i)
ight)$$

• Time elapse:

$$T_{\delta}
ightarrow \left((\underline{l'} = \underline{l}) \wedge (t' - t > 0) \wedge \bigwedge_{x_i \in X} (x'_i - x_i \leq c_i^M(t' - t) + b_i^M) \wedge (x'_i - x_i \geq c_i^M(t' - t) + b_i^M) \wedge \bigwedge_{a \in \Sigma} \underline{c}_a^M(t' - t) + \underline{c}_a^M(t'$$

$$(\underline{l}')_{null} \rightarrow \left((\underline{l}' = \underline{l}) \land (t' = t) \land \bigwedge_{x_i \in X} (x_i' = x_i) \land \bigwedge_{a \in \Sigma} \neg \underline{a} \right)$$

Transition relation T(s, s'):

Switches:

$$\bigwedge_{T\stackrel{\mathsf{def}}{=}\langle l_i, a, \varphi, \lambda, l_j\rangle \in E} T \to \left(\underline{l_i} \wedge \underline{a} \wedge \varphi \wedge \underline{l_j'} \wedge (t' = t) \wedge \bigwedge_{x_i \in X} (x_i' := c_i) \right)$$

• Time elapse:

$$T_{\delta} \to \left((\underline{l'} = \underline{l}) \land (t' - t > 0) \land \bigwedge_{x_i \in X} (x'_i - x_i \le c_i^M(t' - t) + b_i^M) \land (x'_i - x_i \ge c_i^M(t' - t) + b_i^M) \land \bigwedge_{a \in \Sigma} \underline{a}_{\delta} \right)$$

$$(\underline{l'}_{null} o \left((\underline{l'} = \underline{l}) \wedge (t' = t) \wedge \bigwedge_{x_i \in X} (x'_i = x_i) \wedge \bigwedge_{a \in \Sigma} \neg \underline{a} \right)$$

Transition relation T(s, s'):

Switches:

$$\bigwedge_{T\stackrel{\mathsf{def}}{=}\langle l_i, a, \varphi, \lambda, l_j\rangle \in E} T \to \left(\underline{l_i} \land \underline{a} \land \varphi \land \underline{l_j'} \land (t' = t) \land \bigwedge_{x_i \in X} (x_i' := c_i) \right)$$

• Time elapse:

$$T_{\delta}
ightarrow \left((\underline{l'} = \underline{l}) \wedge (t' - t > 0) \wedge \bigwedge_{x_i \in X} (x'_i - x_i \leq c_i^M(t' - t) + b_i^M) \wedge (x'_i - x_i \geq c_i^M(t' - t) + b_i^M) \wedge \bigwedge_{a \in \Sigma} \underline{a} \right)$$

$$\frac{1}{null}
ightarrow \left((\underline{l'} = \underline{l}) \wedge (t' = t) \wedge \bigwedge_{x_i \in X} (x'_i = x_i) \wedge \bigwedge_{a \in \Sigma} \neg \underline{a} \right)$$

Transition relation T(s, s'):

Switches:

$$\bigwedge_{T\stackrel{\mathsf{def}}{=}\langle l_i, a, \varphi, \lambda, l_j\rangle \in E} T \to \left(\underline{l_i} \land \underline{a} \land \varphi \land \underline{l_j'} \land (t' = t) \land \bigwedge_{x_i \in X} (x_i' := c_i) \right)$$

• Time elapse:

$$T_{\delta}
ightarrow \left((\underline{l'} = \underline{l}) \wedge (t' - t > 0) \wedge \bigwedge_{x_i \in X} (x'_i - x_i \leq c_i^M(t' - t) + b_i^M) \wedge (x'_i - x_i \geq c_i^M(t' - t) + b_i^M) \wedge \bigwedge_{a \in \Sigma} \underline{a} \right)$$

$$T_{null}^{j} o \left((\underline{l'} = \underline{l}) \wedge (t' = t) \wedge \bigwedge_{x_i \in X} (x'_i = x_i) \wedge \bigwedge_{a \in \Sigma} \neg \underline{a} \right)$$

Outline

- Motivations & Context
- Background (from previous chapters
- SMT-Based Bounded Model Checking of Timed Systems
 - Basic Ideas
 - Basic Encoding
 - Improved & Extended Encoding
 - A Case-Study
- 4 SMT-Based Bounded Model Checking of Linear Hybrid Systems (hints
- Proposed Exercises

- Consider the Train-gate-controller example from [Alur CAV'99] (see previous chapter)
 - Encode the Initial state formula
 - Encode the transition relation
 - Encode the BMC problem for the formula $\mathbf{G}(s_2 o t_2)$
- As above, reducing the delay time for the controller from 1 to 0.5
 - what happens?
 - in how many steps?
- Encode the above into MathSAT

- Consider the Train-gate-controller example from [Alur CAV'99] (see previous chapter)
 - Encode the Initial state formula
 - Encode the transition relation
 - Encode the BMC problem for the formula $\mathbf{G}(s_2 o t_2)$
- As above, reducing the delay time for the controller from 1 to 0.5
 - what happens?
 - in how many steps?
- Encode the above into MathSAT

- Consider the Train-gate-controller example from [Alur CAV'99] (see previous chapter)
 - Encode the Initial state formula
 - Encode the transition relation
 - Encode the BMC problem for the formula $\mathbf{G}(s_2 o t_2)$
- As above, reducing the delay time for the controller from 1 to 0.5
 - what happens?
 - in how many steps?
- Encode the above into MathSAT

- Consider the rectangular automaton of the Train-gate example (see previous chapter)

 - Encode the Initial state formula $I(s^{(0)})$ Encode the transition relation $R(s^{(i)}, s^{(i+1)})$

