Formal Methods

Module II: Formal Verification

Ch. 10: SMT-Based Model Checking

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Outline

- Motivations & Context
- Background (from previous chapters)
- SMT-Based Bounded Model Checking of Timed Systems
 - Basic Ideas
 - Basic Encoding
 - Improved & Extended Encoding
 - A Case-Study
- SMT-Based Bounded Model Checking of Linear Hybrid Systems (hints)
- Proposed Exercises

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- Model Checking for Timed Systems:
 - relevant improvements and results over the last decades
 - historically, "explicit-state" search style, based on DBMs
 - notable examples: Kronos, Uppaal
 - More recently, symbolic verification techniques:
 - extensions of decision diagrams
 - CDD, DDD, RED, ...
- Key problem: potential blow up in size
- A more recent and viable alternative to Binary Decision Diagrams: SAT-based MC
 - Bounded Model Checking (BMC), K-induction, IC3/PDR, ...

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[Audemard et al. 2002], [Sorea, MTCS'02], [Niebert et al.,FTRTFT'02]

Leverage the SAT-based BMC approach to Timed Systems by means of SMT Solvers

Extensions

- SMT eventually applied to other SAT-based MC techniques
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 - hvbrid systems
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 - hardware verification
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Bounded Model Checking [Biere et al., TACAS'99]

- Given a Kripke Structure M, an LTL property f and an integer bound k, is there an execution path of M of length (up to) k satisfying f? ($M \models_k Ef$)
- Problem converted into the satisfiability of the Boolean formula:

$$[[M]]_{k}^{f} := I(s^{(0)}) \wedge \bigwedge_{i=0}^{k-1} R(s^{(i)}, s^{(i+1)}) \wedge (\neg L_{k} \wedge [[f]]_{k}^{0}) \vee \bigvee_{l=0}^{k} ({}_{l}L_{k} \wedge {}_{l}[[f]]_{k}^{0})$$

s.t.
$$_{l}L_{k}\stackrel{\text{def}}{=} R(s^{(k)}, s^{(l)}), L_{k}\stackrel{\text{def}}{=} \bigvee_{l=0}^{k} {}_{l}L_{k}$$

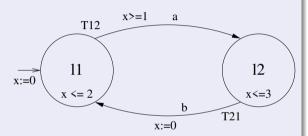
- A satisfying assignment represents a satisfying execution path.
- Test repeated for increasing values of k
- Incomplete
- Very effective for debugging, alternative to OBDDs
- Complemented with K-Induction [Sheeran et al. 2000]
- Further developments: IC3/PDR [Bradley, VMCAI 2011]

General Encoding for LTL Formulae

f	$[[f]]_k^i$	$I[[f]]_{k}^{I}$
р	$p^{(i)}$	$\rho^{(i)}$
$\neg p$	$\neg p^{(i)}$	$\neg p^{(i)}$
$h \wedge g$	$[[h]]_k^i \wedge [[g]]_k^i$	$I[[h]]_{K}^{i} \wedge I[[g]]_{K}^{i}$
h∨g	$[[h]]_k^{\hat{i}} \vee [[g]]_k^{\hat{i}}$	$I_{[[h]]_{K}^{i}} \vee I_{[[g]]_{K}^{i}}$
Хg	$[[g]]_k^{i+1} \text{if } i < k$	$\int_{I} [g]_{k}^{i+1}$ if $i < k$
	\perp otherwise.	$I_{[g]_{k}}^{T}$ otherwise.
G g	1	$\bigwedge_{j=\min(i,l)}^k I[[g]]_k^j$
F g	$\bigvee_{j=i}^{k} [[g]]_{k}^{j}$	$\bigvee_{j=\min(i,l)}^{k} {}_{l}[[g]]_{k}^{j}$
h U g	$\bigvee_{j=i}^{k} \left([[g]]_{k}^{j} \wedge \bigwedge_{n=i}^{j-1} [[h]]_{k}^{n} \right)$	$\bigvee_{j=i}^{k} \left({}_{I}[[g]]_{k}^{j} \wedge \bigwedge_{n=i}^{j-1} {}_{I}[[h]]_{k}^{n} \right) \vee$
	,	$\bigvee_{j=l}^{i-1} \left({}_{i}[[g]]_{k}^{j} \wedge \bigwedge_{n=i}^{k} {}_{i}[[h]]_{k}^{n} \wedge \bigwedge_{n=l}^{j-1} {}_{i}[[h]]_{k}^{n} \right)$
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		$\bigvee_{j=i}^k \left({}_{I}[[h]]_k^j \wedge \bigwedge_{n=i}^j {}_{I}[[g]]_k^n \right) \vee$
		$\bigvee_{j=l}^{l-1} \left(\prod_{i=0}^{l} \prod_{k=0}^{l} \bigwedge_{n=l}^{k} \prod_{i=0}^{l} \prod_{k=0}^{l} \prod_{i=0}^{l} \prod_{i=0}^{l}$

Timed Automata [Alur and Dill, TCS'94; Alur, CAV'99]

- Clocks: real variables (ex. x)
- Locations:
 - label: (ex. *l*₁),
 - invariants: (conjunctive) constraints on clocks values (ex. $x \le 2$)
- Switches:
 - event labels (ex. a),
 - clock constraints (ex. $x \ge 1$),
 - reset statements (ex. x := 0)
- Time elapse: all clocks are increased by the same amount



\mathcal{LRA} -Formulae

[Audemard et al., CADE'02]; [Sorea, MTCS'02]; [Niebert et al.,FTRTFT'02]

- LRA-formulae are Boolean combinations of
 - Boolean variables and
 - linear constraints over real variables (equalities and differences)

• e.g.,
$$(x - 2 \cdot y \ge 4) \land ((x = y) \lor \neg A)$$

- An interpretation \mathcal{I} for a \mathcal{LRA} formula assigns
 - truth values to Boolean variables
 - real values to numerical variables and constants

• e.g.,
$$\mathcal{I}(x) = 3$$
, $\mathcal{I}(y) = -1$, $\mathcal{I}(A) = \bot$

- \mathcal{I} satisfies a \mathcal{LRA} -formula ϕ , written " $\mathcal{I} \models \phi$ ", iff $\mathcal{I}(\phi)$ evaluates to true under the standard semantics of Boolean and mathematical operators.
 - E.g., $\mathcal{I}((x-2 \cdot y \ge 4) \wedge ((x=y) \vee \neg A)) = \top$



The MATHSAT Solver [Audemard et al., CADE'02]

- Bottom level: a \mathcal{T} -Solver for sets of \mathcal{LRA} constraints
 - E.g. $\{..., z_1 x_1 \le 6, z_2 x_2 \ge 8, x_1 = x_2, z_1 = z_2, ...\} \Longrightarrow unsat.$
 - Combination of symbolic and numerical algorithms (equivalence class building, Belman-Ford, Simplex)
- Top level: a CDCL procedure for propositional satisfiability
 - mathematical predicates treated as propositional atoms
 - ullet invokes $\mathcal{T} ext{-Solver}$ on every assignment found
 - used as an enumerator of assignments
 - lots of enhancements

(see chapter on SMT)

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SMT-Based BMC for Timed Systems

Independently developed approaches (2002):

- [Audemard et al. FORTE'02]: encoding into LRA
 - all LTL properties
- [Sorea, MTCS'02]: encoding into LRA
 - based on automata-theoretic approach for LTL
- Niebert et al.,FTRTFT'02]: encoding into DL
 - limited to reachability

Disclaimer

These slides are adapted from [Audemard et al. FORTE'02]:

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BMC for Timed Systems

Basic ingredients:

- An extension of propositional logic expressive enough to represent timed information: " \mathcal{LRA} -formulae"
- A SMT(\mathcal{LRA}) solver for deciding \mathcal{LRA} -formulae \Longrightarrow e.g., the MATHSAT solver
- \bullet An encoding from timed BMC problems into $\mathcal{LRA}\text{-}formulae$
 - ullet \mathcal{LRA} -satisfiable iff an execution path within the bound exists

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The encoding

Given a timed automaton A and a LTL formula f:

• The encoding $[[A, f]]_k$ is obtained following the same schema as in propositional BMC:

$$[[A, f]]_k := I(s^{(0)}) \wedge \bigwedge_{i=0}^{k-1} R(s^{(i)}, s^{(i+1)}) \wedge (\neg L_k \wedge [[f]]_k^0) \vee \bigvee_{l=0}^k ({}_{l}L_k \wedge {}_{l}[[f]]_k^0)$$

- $[[M, f]]_k$ is a \mathcal{LRA} -formula, where
 - Boolean variables encode the discrete part of the state of the automaton
 - constraints on real variables represent the temporal part of the state

- Locations: an array \underline{I} of $n \stackrel{\text{def}}{=} \lceil log_2(|L|) \rceil$ Boolean variables
 - I_i holds iff the system is in the location I_i
 - ex: " $\neg \underline{l_i}[3] \land \underline{l_i}[2] \land \neg \underline{l_i}[1] \land \underline{l_i}[0]$ " means "the system is in location $\underline{l_5}$ "
 - " $(\underline{l_i} = \overline{l_j})$ " stands for " $\bigwedge_n (\underline{l_i}[n] \leftrightarrow l_j[n])$ ",
 - "primed" variables $\underline{l_i}$ to represent location after transition
- Events: for each event $a \in \Sigma$, a Boolean variable <u>a</u>
 - <u>a</u> holds iff the system executes a switch with event a.
- Switches: for each switch $\langle I_i, a, \varphi, \lambda, I_j \rangle \in E$, a Boolean variable T,
 - T holds iff the system executes the corresponding switch
- Time elapse and null transitions: two variables T_{δ} and T_{null}^{j}
 - T_{δ} holds iff time elapses by some $\delta > 0$
 - T_{null}^{j} holds if and only A_{j} does nothing (specific for automaton A_{j})

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- Clocks values x are "normalized" wrt absolute time (t x):
 - a clock value x is written as difference t x
 - t represents the absolute time
 - "offset" variable x represents the absolute time when the clock was reset last time
- Clock constraints reduce to $(t x \bowtie c)$, $\bowtie \in \{\le, \ge, <, >\}$, $c \in \mathbb{Z}$
- Clock reset conditions reduce to (x := t)
- Clock equalities like $(x_k = x_l)$ reduce to $(t_k x_k = t_l x_l)$
 - appear only in loops
 - only place where full \mathcal{LRA} is needed (rather than \mathcal{DL})
 - \implies for invariant checking (no loops) \mathcal{DL} suffices
- Encoding the effect of transitions:
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- Clocks values x are "normalized" wrt absolute time (t x):
 - a clock value x is written as difference t x
 - t represents the absolute time
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- Clock constraints reduce to $(t x \bowtie c)$, $\bowtie \in \{\le, \ge, <, >\}$, $c \in \mathbb{Z}$
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- Clock equalities like $(x_k = x_l)$ reduce to $(t_k x_k = t_l x_l)$
 - appear only in loops
 - only place where full \mathcal{LRA} is needed (rather than \mathcal{DL})
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- Encoding the effect of transitions:
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 - otherwise:
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Initial condition I(s):

Initially, the automaton is in an initial location:

$$\bigvee_{l_i \in L^0} \underline{l_i}$$

Initially, clocks have a null value:

$$\bigwedge_{x \in X} (x = t)$$

Remark

- in particular when encoding symbolically the discrete part of the system
- ullet e.g., there is probably a much more compact formula equivalent to $\bigvee_{l_i \in L^0} \underline{l_i}$



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Encoding: Invariants

Transition relation R(s, s'): Invariants

• Always, being in a location implies the corresponding invariant constraints:

$$\bigwedge_{l_i \in L} (\underline{l_i} \to \bigwedge_{\psi \in I(l_i)} \psi),$$

Transition relation T(s, s'):

Switches:

$$\bigwedge_{T \stackrel{\mathsf{def}}{=} \langle l_i, a, \varphi, \lambda, l_j \rangle \in E} \mathcal{T} \rightarrow \underbrace{\left(\underline{l_i} \land \underline{a} \land \varphi \land \underline{l_j'} \land (t' = t) \land \bigwedge_{x \in \lambda} (x' = t') \land \bigwedge_{x \not\in \lambda} (x' = x)\right)}_{X \not\in \lambda}$$

• Time elapse:

$$T_{\delta} \to \left((\underline{l'} = \underline{l}) \land (t' - t > 0) \land \bigwedge_{x \in X} (x' = x) \land \bigwedge_{a \in \Sigma} \neg \underline{a} \right)$$

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Mutual exclusion between events:

$$\bigwedge_{a_k,a_r\in\Sigma,a_k\neq a_r}(\neg\underline{a}_k\vee\neg\underline{a}_r)$$

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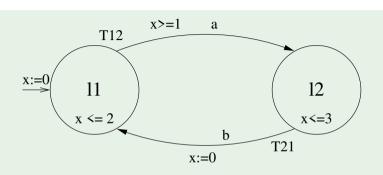
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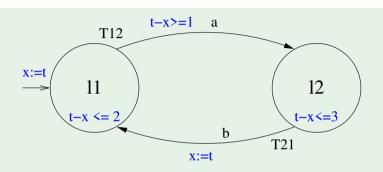
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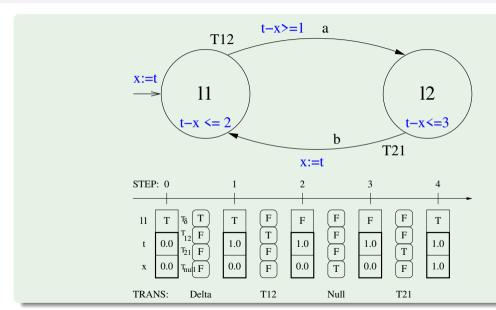
A Simple Example



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Outline

- Motivations & Context
- Background (from previous chapters)
- SMT-Based Bounded Model Checking of Timed Systems
 - Basic Ideas
 - Basic Encoding
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Adding Global Variables

Dealing with some global variable v on discrete domain:

- A switch $T \stackrel{\text{def}}{=} \langle I_i, a, \varphi, \lambda, I_j \rangle$ can
 - be subject to a condition $\psi(v)$
 - \implies add $T \rightarrow \psi(v)$
 - assign v to some value n or keep its value

$$\implies$$
 add $T \rightarrow (v' = n)$ or add $T \rightarrow (v' = v)$

• T_{δ} mantains the value of v:

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 add $T_{\delta} \rightarrow (v'=v)$

• T_{null}^{I} imposes no constraint on v:

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MATHSAT: Optimizations

Customization of MATHSAT

• Limit Boolean variable-selection heuristic to pick transition variables, in forward order

Encoding: Optimizations

Boolean Propagation of Math Constraints:

Idea: add small and mathematically-obvious lemmas

- ⇒ force assignments by unit-propagation,
- \Longrightarrow saves calls to the \mathcal{T} -Solvers

Encoding Variants

Shortening counter-examples:

- Collapsing consequent time elapsing transitions:
 - $s \stackrel{\delta}{\longmapsto} s, s \stackrel{\delta'}{\longmapsto} s$ reduced to $s \stackrel{\delta+\delta'}{\longmapsto} s$
 - add $\neg T_{\delta} \lor \neg T'_{\delta}$ to transition relation R(s, s')
 - ⇒ implements the notion of "non-Zeno-ness" (see previous chapter)
- Allow multiple parallel transitions
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Remark: may change the notion of "next step" ⇒ only if no "X" operators occurs in property!

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Encoding Variants (cont.)

A limited form of symmetry reduction

If N automata are symmetric (frequent with protocol verification):

- Intuition: restrict executions s.t.
 - At step 0 only A₀ can move
 - At step 1 only A₀, A₁ can move
 - At step 2 only A_0, A_1, A_2 can move
 - ...
 - ⇒ we name "0" the first automata who acts, "1" the second one, etc.
- for step i < N-1, we drop the disjunct $\neg T_{null}^{i+1}$ $(i) \lor \ldots \lor \neg T_{null}^{N-1}$:

set
$$\bigvee_{j=0}^{min(i,N-1)} \neg T_{null}^{j\ (i)}$$
 rather than $\bigvee_{j=0}^{N-1} \neg T_{null}^{j\ (i)}$

- \implies drops "symmetric" executions
- \implies reduces the search space of a up to $2^{N(N-1)/2}$ factor!

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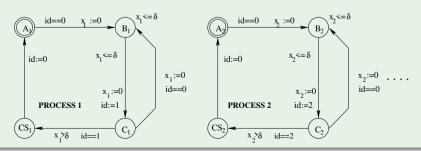
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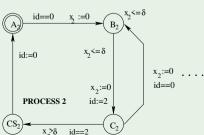
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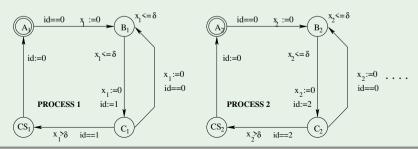
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- shared variable $id \in \{0, 1, 2, ..., N\}$: process identifier (0: none)
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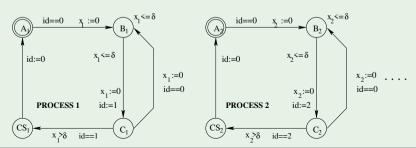
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 - Reachability: EF A, P_i, C (reached in N+1 steps)
 - Fairness: $E \neg (GFP_i.B \rightarrow GFP_i.CS)$ (reached in N+5 step



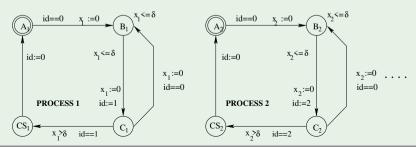
- N identical processes accessing one critical section
- shared variable $id \in \{0, 1, 2, ..., N\}$: process identifier (0: none)
 - when entering wait state C_j , agent A_j writes its code on id
 - if id = j after δ , then A_i can enter the critical session
- Two properties under test
 - Reachability: **EF** $\bigwedge_i P_i.C$ (reached in N+1 steps)
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Fischer's protocol: (cont.)

Exercise:

- Why is $\mathbf{EF} \wedge_i P_i \cdot C$ reached in N+1 steps?
- Why is $\mathbf{E} \neg (\mathbf{GF}P_i.B \rightarrow \mathbf{GF}P_i.CS)$ reached in N+5 steps?

(See [Audemard et al, FORTE'02] for the solution.)

Fischer's protocol: (reachability)

$M \models_k \mathbf{EF} \bigwedge_i P_i.C$

	Матн	SATI	Матн	SAT,Sym	DE	D	UPF	PAL	Kroi	NOS	RE	D	RED,	Sym
N	Time	Size	Time	Size	Time	Size	Time	Size	Time	Size	Time	Size	Time	Size
3	0.05	2.9	0.04	2.9	0.11	106	0.01	1.7	0.01	0.8	0.23	2.0	0.19	2.0
4	0.09	3.0	0.08		0.14		0.02					2.1	0.70	
5	0.20	3.2	0.16		0.24		0.21					2.2		
6	0.60	3.7			0.47		3.44		0.39		12.00			
7	3.20	4.2			1.30		153			MEM		4.0		
8	29	4.9	0.52		3.96		TIME				121	7.6		
9	343	5.9		5.9							416			
10		6.5	1.01	6.5							1382	39		23
	TIME		1.39	7.0		106					TIME		157	38
12		l	1.89	7.5		MEM							266	63
13			2.44	8.2									439	
14			3.24	8.9									709	
15			4.11	9.7									1118	
16			5.10	10.7									1717	342
17			6.30	11.7									2582	492
18			8.00	12.9									TIME	
19			9.50	14.2										

(MATHSAT times are sum of all instances up to k)

Fischer's protocol (liveness violation)

$$M \models_{k} \mathbf{E} \neg (\mathbf{GF}P_{i}.B \rightarrow \mathbf{GF}P_{i}.CS)$$

			MATH:	SAT		MATHSAT with Boehm heuristic					
$k \setminus N$	2	3	4	5	6	2	3	4	5	6	
2	0.01	0.01	0.01	0.01	0.02	0.01	0.01	0.01	0.01	0.02	
3	0.01	0.02	0.01	0.01	0.03	0.01	0.01	0.02	0.03	0.04	
4	0.01	0.02	0.02	0.02	0.04	0.01	0.02	0.04	0.07	0.17	
5	0.02	0.03	0.05	0.09	0.18	0.01	0.03	0.09	0.30	1.16	
6	0.03	0.10	0.21	0.54	1.35	0.02	0.07	0.31	1.52	7.74	
7	0.04	0.26	0.97	3.20	9.83	0.02	0.18	1.19	7.14	45.00	
8		0.65	4.80	19.72	70.70		0.06	4.70	33.50	242.00	
9			5.55	112.17	478.00			0.61	165.90	1348.00	
10				303.17	3086.00				9.92	7824.00	
11					5002.00					252.00	
Σ	0.12	1.08	11.62	438.93	8648.15	0.07	0.37	6.98	218.40	9720.13	

Outline

- Motivations & Context
- Background (from previous chapters)
- SMT-Based Bounded Model Checking of Timed Systems
 - Basic Ideas
 - Basic Encoding
 - Improved & Extended Encoding
 - A Case-Study
- SMT-Based Bounded Model Checking of Linear Hybrid Systems (hints)
- Proposed Exercises



The encoding

Given a Linear hybrid automaton A and a LTL formula f:

• The encoding $[[A, f]]_k$ is obtained following the same schema as in propositional BMC:

$$[[A, f]]_k := I(s^{(0)}) \wedge \bigwedge_{i=0}^{k-1} R(s^{(i)}, s^{(i+1)}) \wedge (\neg L_k \wedge [[f]]_k^0) \vee \bigvee_{l=0}^k ({}_{l}L_k \wedge {}_{l}[[f]]_k^0)$$

- $[[M, f]]_k$ is a \mathcal{LRA} -formula, where
 - Boolean variables encode the discrete part of the state of the automaton
 - a real variable *t* (rational for rectangular automata) encodes absolute time elapse
 - real (rational) variables $x \in X$ encode continuous variables
 - constraints on real (rational) variables represent the continuous flow part of the state

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Encoding: Boolean Variables

- Locations: I, as with timed systems
- Events: $a \in \Sigma$, as with timed systems
- Switches: T, as with timed systems
- Time elapse and null transitions: T_{δ} and T_{null}^{j} , as with timed systems

- Continuous variables:
 - t represents the absolute time
 - real (rational) variables x represent continuous values
- Continuous constraints (initial, guards, invariants) reduce to linear constraints on X: $\sum_{x_i \in X} a_i x_i \bowtie c$ s.t. $\bowtie \in \{\leq, \geq, <, >\}, c \in \mathbb{Q}$
- Encoding the effect of discrete transitions:
 - ullet r=t, absolute time does not elapse ullet Jumo relations reduce to linear transformations: Λ \mathcal{M} :=
- Encoding the effect of time-elapse transitions:
 - t' > t• $\bigwedge_j \Psi_j(X, t, X', t) \ge 0$
 - where $\Psi_l(X,t,X',t) \stackrel{\text{def}}{=} \sum_i a_{ij}(x_i'-x_i) + c(t'-t) \geq 0$, given $\bigwedge_i \sum_i a_{ij} \frac{\partial x_i}{\partial t} + c \geq 0$

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- $x_i \bowtie c_i$ with rectangular automata
- Encoding the effect of discrete transitions:
 - t'=t absolute time does not elapse
 - \bullet Jump relations reduce to linear transformations: $\bigwedge, \chi'_i := \sum_i a_{ij} x_i + \alpha_i x_i$
- Encoding the effect of time-elapse transitions:
 - $\bullet t' > t$
 - $\bullet \ \Lambda_i \Psi_i(X,t,X',t) > 0$
 - where $\Psi_l(X,t,X',t) \cong \sum_i a_i(x_i'-x_i) + c(t'-t) \geq 0$, given $\bigwedge_i \sum_i a_i \stackrel{\text{def}}{\longrightarrow} + c \geq 0$

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Initial condition I(s):

• Initially, the automaton is in an initial location:

$$t=0 o \bigvee_{I_i \in L^0} \underline{I_i}$$

Initially, clocks comply with initial conditions:

$$t = 0 \to \bigwedge_{l_i \in L^0} (\underline{l_i} \to \mathit{Init}_l(X))$$

Transition relation R(s, s'): Invariants

• Always, being in a location implies the corresponding invariant constraints:

$$\bigwedge_{l_i \in L} (\underline{l_i} \to \bigwedge_{\psi \in I(l_i)} \psi),$$

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Encoding (linear automata): Transitions

Transition relation T(s, s'):

Switches:

$$\bigwedge_{T \stackrel{\mathsf{def}}{=} \langle l_i, a, \varphi, J, l_j \rangle \in E} T \rightarrow \left(\underline{l_i} \land \underline{a} \land \varphi \land \underline{l_j'} \land (t' = t) \land \bigwedge_{x_j \in X} (x_j' := \sum_i a_{ij} x_i + c_j) \right)$$

• Time elapse:

$$T_{\delta} \to \left((\underline{l'} = \underline{l}) \wedge (t' - t > 0) \wedge (\bigwedge_{j} \Psi_{j}(X, t, X', t) \geq 0) \wedge \bigwedge_{a \in \Sigma} \neg \underline{a} \right)$$

• Null transition:

$$\frac{d}{dt} au_{null} o \left((\underline{l'} = \underline{l}) \wedge (t' = t) \wedge \bigwedge_{x_i \in X} (x'_i = x_i) \wedge \bigwedge_{a \in \Sigma} \neg \underline{a} \right)$$

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• Time elapse:

$$T_{\delta} \to \left((\underline{l'} = \underline{l}) \land (t' - t > 0) \land \bigwedge_{x_i \in X} (x'_i - x_i \le c_i^M(t' - t) + b_i^M) \land (x'_i - x_i \ge c_i^M(t' - t) + b_i^M) \land \bigwedge_{a \in \Sigma} \underline{a}_{\delta} \right)$$

$$(\underline{l'}_{null} o \left((\underline{l'} = \underline{l}) \wedge (t' = t) \wedge \bigwedge_{x_i \in X} (x'_i = x_i) \wedge \bigwedge_{a \in \Sigma} \neg \underline{a} \right)$$

Transition relation T(s, s'):

Switches:

$$\bigwedge_{T\stackrel{\mathsf{def}}{=}\langle l_i, a, \varphi, \lambda, l_j\rangle \in E} T \to \left(\underline{l_i} \land \underline{a} \land \varphi \land \underline{l_j'} \land (t' = t) \land \bigwedge_{x_i \in X} (x_i' := c_i) \right)$$

• Time elapse:

$$T_{\delta}
ightarrow \left((\underline{l'} = \underline{l}) \wedge (t' - t > 0) \wedge \bigwedge_{x_i \in X} (x'_i - x_i \leq c_i^M(t' - t) + b_i^M) \wedge (x'_i - x_i \geq c_i^M(t' - t) + b_i^M) \wedge \bigwedge_{a \in \Sigma} \underline{a} \right)$$

$$\frac{1}{null}
ightarrow \left((\underline{l'} = \underline{l}) \wedge (t' = t) \wedge \bigwedge_{x_i \in X} (x'_i = x_i) \wedge \bigwedge_{a \in \Sigma} \neg \underline{a} \right)$$

Transition relation T(s, s'):

Switches:

$$\bigwedge_{T\stackrel{\mathsf{def}}{=}\langle l_i, a, \varphi, \lambda, l_j\rangle \in E} T \to \left(\underline{l_i} \land \underline{a} \land \varphi \land \underline{l_j'} \land (t' = t) \land \bigwedge_{x_i \in X} (x_i' := c_i) \right)$$

• Time elapse:

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$$T_{null}^{j} o \left((\underline{l'} = \underline{l}) \wedge (t' = t) \wedge \bigwedge_{x_i \in X} (x'_i = x_i) \wedge \bigwedge_{a \in \Sigma} \neg \underline{a} \right)$$

Outline

- Motivations & Context
- Background (from previous chapters
- SMT-Based Bounded Model Checking of Timed Systems
 - Basic Ideas
 - Basic Encoding
 - Improved & Extended Encoding
 - A Case-Study
- 4 SMT-Based Bounded Model Checking of Linear Hybrid Systems (hints
- Proposed Exercises

- Consider the Train-gate-controller example from [Alur CAV'99] (see previous chapter)
 - Encode the Initial state formula
 - Encode the transition relation
 - Encode the BMC problem for the formula $\mathbf{G}(s_2 o t_2)$
- As above, reducing the delay time for the controller from 1 to 0.5
 - what happens?
 - in how many steps?
- Encode the above into MathSAT

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- Consider the rectangular automaton of the Train-gate example (see previous chapter)

 - Encode the Initial state formula $I(s^{(0)})$ Encode the transition relation $R(s^{(i)}, s^{(i+1)})$

