Introduction to Formal Methods Chapter 11: Timed and Hybrid Systems

Roberto Sebastiani

DISI, Università di Trento, Italy - roberto.sebastiani@unitn.it URL: http://disi.unitn.it/rseba/DIDATTICA/fm2020/Teaching assistant: Enrico Magnago - enrico.magnago@unitn.it

CDLM in Informatica, academic year 2019-2020

last update: Sunday 24th May, 2020, 20:10

Copyright notice: some material (text, figures) displayed in these slides is courtesy of R. Alur, M. Benerecetti, A. Cimatti, M. Di Natale, P. Pandya, M. Pistore, M. Roveri, and S. Tonetta, who detain its copyright. Some exampes displayed in these slides are taken from (Clarke, Grunberg & Peled, "Model Checking", MIT Press), and their copyright is detained by the authors. All the other material is copyrighted by Roberto Sebastiani. Every commercial use of this material is strictly forbidden by the copyright laws without the authorization of the authors. No copy of these slides can be displayed in public without containing this copyright rowing the control of the commercial use of the slides can be displayed in public without containing this copyright notice.

Outline

- Motivations
- Timed systems: Modeling and Semantics
 - Timed automata
- Symbolic Reachability for Timed Systems
 - Making the state space finite
 - Region automata
 - Zone automata
- 4 Hybrid Systems: Modeling and Semantics
 - Hybrid automata
- 5 Symbolic Reachability for Hybrid Systems
 - Multi-Rate and Rectangular Hybrid Automata
 - Linear Hybrid Automata
- 6 Exercises

Acknowledgments

Thanks for providing material to:

- Rajeev Alur & colleagues (Penn University)
- Paritosh Pandya (IIT Bombay)
- Andrea Mattioli, Yusi Ramadian (Univ. Trento)
- Marco Di Natale (Scuola Superiore S.Anna, Italy)

Disclaimer

- very introductory
- very-partial coverage
- mostly computer-science centric

Hybrid Modeling

Hybrid machines = State machines + Dynamic Systems



Hybrid Modeling: Examples

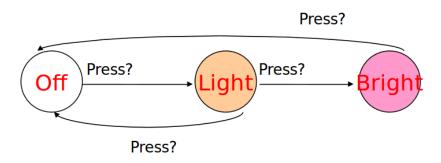


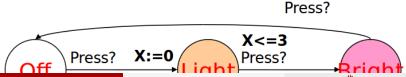
- Automotive Applications
- Vehicle Coordination Protocols

Timed Automata



Example: Simple light control

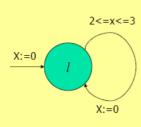


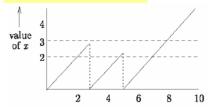


Modeling: timing constraints

Finite graph + finite set of (real-valued) clocks

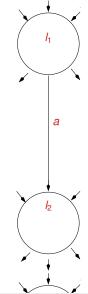
- Vertexes are locations
 - Time can elapse there
 - Constraints (invariants)
- Edges are switches
 - Subject to constraints
 - Reset clocks





Meaning of clock value: time elapsed since the last time it was reset.

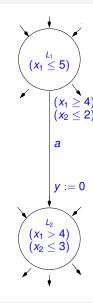
Timed Automata



- Locations $l_1, l_2, ...$ (like in standard automata)
 - discrete part of the state

Timed Automata: States and Transitions

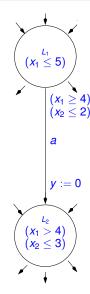
- State: $\langle I_i, x_1, x_2 \rangle$
 - $\langle I_1, 4, 7 \rangle$: OK!
 - $\langle l_2, 2, 4 \rangle$: not OK! (violates invariant in l_2)
- Switch: $\langle I_i, x, y \rangle \xrightarrow{a} \langle I_i, x', y' \rangle$
 - $\langle I_1, 4.5, 2 \rangle \xrightarrow{a} \langle I_2, 4.5, 0 \rangle$: OK!
 - $\langle I_1, 6, 2 \rangle \xrightarrow{a} \langle I_2, 6, 0 \rangle$: not OK! (violates invar. in I_1)
 - $\langle I_1, 3, 2 \rangle \xrightarrow{a} \langle I_2, 3, 0 \rangle$: not OK! (violates guard)
 - $\langle I_1, 4.5, 2 \rangle \xrightarrow{a} \langle I_2, 4.5, 2 \rangle$: not OK! (violates reset)
 - $\langle l_1, 4, 2 \rangle \xrightarrow{a} \langle l_2, 4, 0 \rangle$: not OK! (violates invar. in l_2)
- Wait (time elapse): $\langle I_i, x, y \rangle \xrightarrow{\delta} \langle I_i, x + \delta, y + \delta \rangle$
 - $\langle I_1, 3, 0 \rangle \xrightarrow{2} \langle I_1, 5, 2 \rangle$: OK!
 - $\langle I_1, 3, 0 \rangle \xrightarrow{3} \langle I_1, 6, 3 \rangle$: not OK! (violates invar. in I_1)



Timed Automata: Formal Syntax

Timed Automaton $\langle L, L^0, \Sigma, X, \Phi(X), E \rangle$

- L: Set of locations
- $L^0 \subset L$: Set of initial locations
- Σ: Set of labels
- X: Set of clocks
- $\Phi(X)$: Set of invariants
- $E \subseteq L \times \Sigma \times \Phi(X) \times 2^X \times L$: Set of switches A switch $\langle I, a, \varphi, \lambda, I' \rangle$ s.t.
 - /: source location
 - a: label
 - φ: clock constraints
 - $\lambda \subseteq X$: clocks to be reset
 - /': target location



Clock constraints and clock interpretations

• Grammar of clock constraints:

$$\varphi ::= \mathbf{x} \leq \mathbf{C} \mid \mathbf{x} < \mathbf{C} \mid \mathbf{x} \geq \mathbf{C} \mid \mathbf{x} > \mathbf{C} \mid \varphi \wedge \varphi$$

- s.t. C positive integer values.
- ⇒ allow only comparison of a clock with a constant
- clock interpretation: ν

$$X = \langle x, y, z \rangle, \quad \nu = \langle 1.0, 1.5, 0 \rangle$$

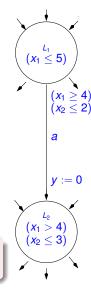
• clock interpretation ν after δ time: $\nu + \delta$

$$\delta = 0.2, \ \nu + \delta = \langle 1.2, 1.7, 0.2 \rangle$$

• clock interpretation ν after reset λ : $\nu[\lambda]$

$$\lambda = \{y\}, \ \nu[y := 0] = \langle 1.0, 0, 0 \rangle$$

A state for a timed automaton is a pair $\langle I, \nu \rangle$, where I is a location and ν is a clock interpretation

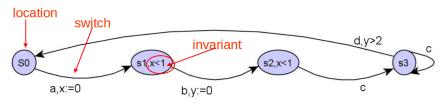


Remark: why integer constants in clock constraints?

The constant in clock constraints are assumed to be integer w.l.o.g.:

- if rationals, multiply them for their greatest common denominator, and change the time unit accordingly
- in practice, multiply by 10^k (resp 2^k), k being the number of precision digits (resp. bits), and change the time unit accordingly Ex: 1.345, 0.78, 102.32 seconds
 - ⇒ 1,345, 780, 102,320 milliseconds

Example



- clocks $\{x, y\}$ can be set/reset independently
- x is reset to 0 from s_0 to s_1 on a
- switches b and c happen within 1 time-unit from a because of constraints in s₁ and s₂
- delay between b and the following d is > 2
- no explicit bounds on time difference between event c − d

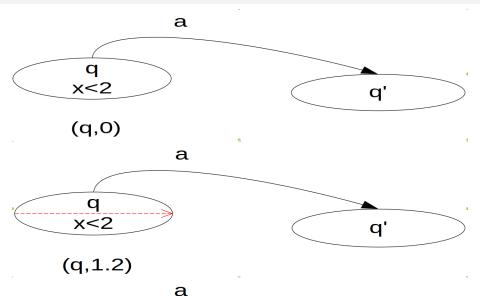
Timed Automata: Semantics

Semantics of A defined in terms of a (infinite) transition system

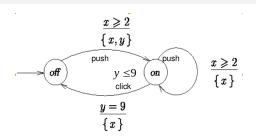
$$S_A \stackrel{\text{def}}{=} \langle Q, Q^0, \rightarrow, \Sigma \rangle$$

- Q: $\{\langle I, \nu \rangle\}$ s.t. I location and ν clock evaluation
- Q^0 : $\{\langle I, \nu \rangle\}$ s.t. $I \in L^0$ location and $\nu(X) = 0$
- →:
 - state change due to location switch
 - state change due to time elapse
- Σ : set of labels of $\Sigma \cup \mathbb{Q}^+$

State change in transition system



Example



- Switch may be turned on whenever at least 2 time units has elapsed since last "turn off"
- Light automatically switches off after 9 time units.

Example execution

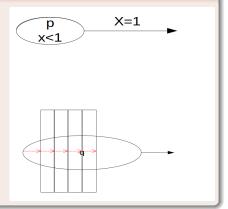
$$\begin{array}{c} \langle \textit{off}, 0, 0 \rangle \xrightarrow{3.5} \langle \textit{off}, 3.5, 3.5 \rangle \xrightarrow{\textit{push}} \langle \textit{on}, 0, 0 \rangle \xrightarrow{3.14} \langle \textit{on}, 3.14, 3.14 \rangle \xrightarrow{\textit{push}} \\ \langle \textit{on}, 0, 3.14 \rangle \xrightarrow{3} \langle \textit{on}, 3, 6.14 \rangle \xrightarrow{2.86} \langle \textit{on}, 5.86, 9 \rangle \xrightarrow{\textit{click}} \langle \textit{on}, 0, 9 \rangle \\ \end{array}$$

Remark: Non-Zenoness

Beware of Zeno! (paradox)

 When the invariant is violated some edge must be enabled

 Automata should admit the possibility of time to diverge

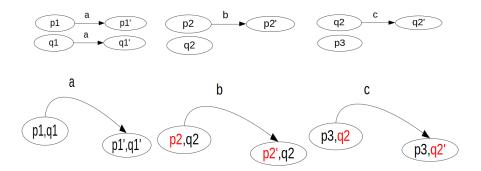


Combination of Timed Automata

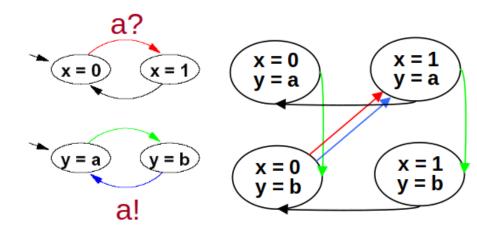
- Complex system = product of interacting systems
- Let $A_1 \stackrel{\text{def}}{=} \langle L_1, L_1^0, \Sigma_1, X_1, \Phi_1(X_1), E_1 \rangle$, $A_2 \stackrel{\text{def}}{=} \langle L_2, L_2^0, \Sigma_2, X_2, \Phi_2(X_2), E_2 \rangle$
- Product: $A_1 || A_2 \stackrel{\text{def}}{=} \langle L_1 \times L_2, L_1^0 \times L_2^0, \Sigma_1 \cup \Sigma_2, X_1 \cup X_2, \Phi_1(X_1) \cup \Phi_2(X_2), E_1 || E_2 \rangle$
- Transition iff:
 - Label a belongs to both alphabets ⇒ synchronized blocking synchronization: a-labeled switches cannot be shot alone
 - Label a only in the alphabet of $A_1 \Longrightarrow asynchronized$
 - Label a only in the alphabet of $A_2 \Longrightarrow$ asynchronized

Transition Product

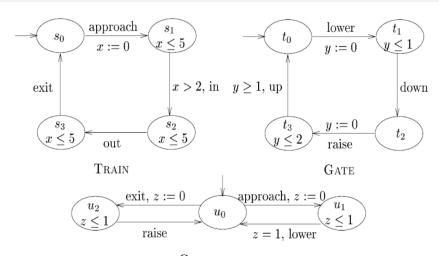
$$\Sigma_1 \stackrel{\text{def}}{=} \{a, b\}$$
 $\Sigma_2 \stackrel{\text{def}}{=} \{a, c\}$



Transition Product: Example



Example: Train-gate controller [Alur CAV'99]

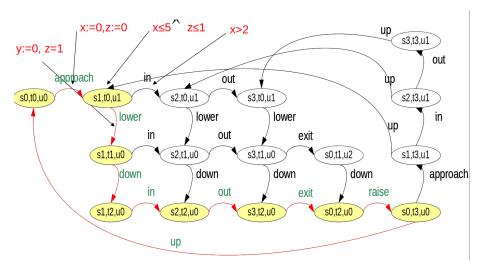


Controller

Desired property: $G(s_2 \rightarrow t_2)$

Ch. 11: Timed and Hybrid Systems

Train-gate controller: Product



26/100

Reachability Analysis

- Verification of safety requirement: reachability problem
- Input: a timed automaton A and a set of target locations $L^F \subseteq L$
- Problem: Determining whether L^F is reachable in a timed automaton A
- A location / of A is reachable if some state q with location component / is a reachable state of the transition system S_A

Timed/hybrid Systems: problem

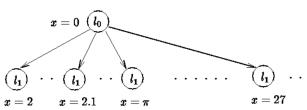
Problem

The system S_A associated to A has infinitely-many states & symbols.

- Is finite state analysis possible?
- Is reachability problem decidable?



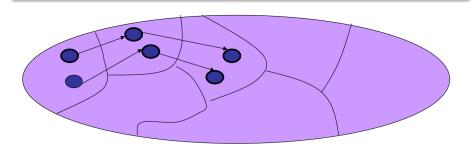
gives rise to the infinite transition system:



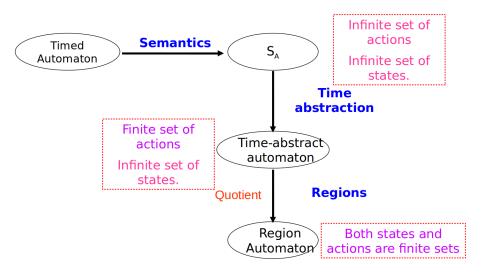
Idea: Finite Partitioning

Goal

Partition the state space into finitely-many equivalence classes, so that equivalent states exhibit (bi)similar behaviors



Reachability analysis



Timed Vs Time-Abstract Relations

Idea

Infinite transition system associated with a timed/hybrid automaton A:

- S_A : Labels on continuous steps are delays in \mathbb{Q}^+
- U_A (time-abstract): actual delays are suppressed all continuous steps have same label
- from "wait δ and switch" to "wait (sometime) and switch"

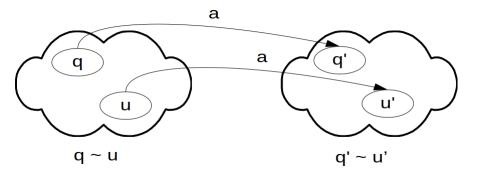
Time-abstract transition system U_A

U_A (time-abstract): actual delays are suppressed

- Only change due to location switch stated explicitly
- Cut system to finitely many labels
- U_A (instead of S_A) allows for capturing untimed properties (e.g., reachability, safety)

```
A: ("wait \delta; switch;")
\langle I_0, 0, 0 \rangle \xrightarrow{1.2} \langle I_0, 1.2, 1.2 \rangle \xrightarrow{a} \langle I_1, 0, 1.2 \rangle \xrightarrow{0.7} \langle I_1, 0.7, 1.9 \rangle \xrightarrow{b} \langle I_2, 0.7, 0 \rangle
S_A: ("wait \delta and switch;")
\langle I_0, 0, 0 \rangle \xrightarrow{1.2+a} \langle I_1, 0, 1.2 \rangle \xrightarrow{0.7+b} \langle I_2, 0.7, 0 \rangle
U_A: ("wait (sometime) and switch;")
\langle I_0, 0, 0 \rangle \xrightarrow{a} \langle I_1, 0, 1.2 \rangle \xrightarrow{b} \langle I_2, 0.7, 0 \rangle
```

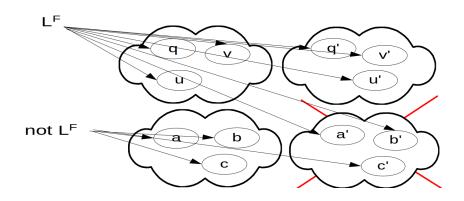
Stable quotients



Idea: Collapse states which are equivalent modulo "wait & switch"

- Cut to finitely many states
- Stable equivalence relation
- Quotient of U_A = transition system $[U_A]$

L^F-sensitive equivalence relation



All equivalent states in a class belong to either L^F or not L^F

• E.g.: states with different labels cannot be equivalent

Stable Quotient: Intuitive example

Task: plan trip from DISI to VR train station

"take the next #5 bus to TN train station and then the 6pm train to VR"

- Constraints:
 - It is 5.18pm
 - Train to VR leaves at TN train station at 6.00pm
 - it takes 3 minutes to walk from DISI to BUS stop
 - Bus #5 passes 5.20pm or at 5.40pm
 - Bus #5 takes 15 minutes to TN train station
 - it takes 2 minutes to walk from BUS stop to TN train station
- Time-Abstract plan (U_A) :
 - "walk to bus stop; take 5.40 #5 bus to TN train-station stop; walk to train station; take the 6pm train to VR"
- Actual (implicit) plan (A):
 - "wait δ_1 ; walk to bus stop; wait δ_2 ; take 5.40 #5 bus to TN train-station stop; wait δ_3 at bus stop; walk to train station; wait δ_4 ; take the 6pm train to VR" where $\delta_1 + \delta_2 = 19$ min and $\delta_3 + \delta_4 = 3$ min
- all executions with distinct values of δ_i are bisimilar

Region Equivalence over clock interpretation

Preliminary definitions & terminology

Given a clock x:

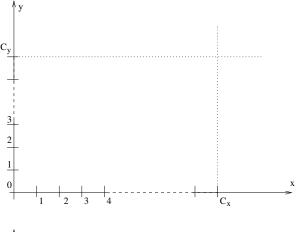
- |x| is the integral part of x (ex: |3.7| = 3)
- fr(x) is the fractional part of x (ex: fr(3.7) = 0.7)
- C_x is the maximum constant occurring in clock constraints $x \bowtie C_x$

Region Equivalence: $\nu \cong \nu'$

Given a timed automaton A, two clock interpretations ν, ν' are region equivalent $(\nu \cong \nu')$ iff all the following conditions hold:

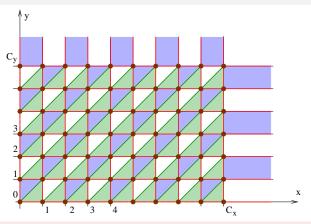
- C1: For every clock x, either $|\nu(x)| = |\nu'(x)|$ or $|\nu(x)|, |\nu'(x)| \geq C_x$
- C2: For every clock pair x, y s.t. $\nu(x), \nu'(x) \leq C_x$ and $\nu(y), \nu'(y) \leq C_y$, $fr(\nu(x)) < fr(\nu(y))$ iff $fr(\nu'(x)) < fr(\nu'(y))$
- C3: For every clock x s.t. $\nu(x), \nu'(x) \leq C_x$ $fr(\nu(x)) = 0$ iff $fr(\nu'(x)) = 0$

Conditions: C1 + C2 + C3





Regions, intuitive idea:

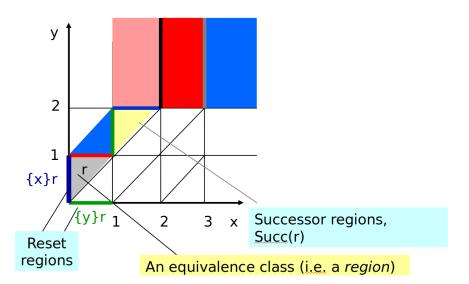


Intuition: $\nu \cong \nu'$ iff they satisfy the same set of constraints in the form

$$X_i < C, X_i > C, X_i = C, X_i - X_i < C, X_i - X_i > C, X_i - X_i = C$$

s.t. $c \leq C_{x_i}$

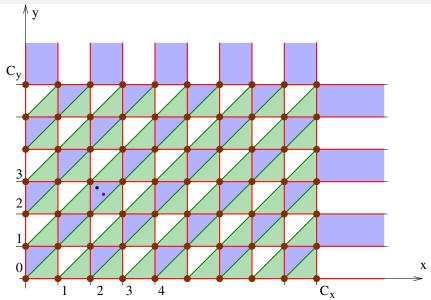
Region Operations



Properties of Regions

- The region equivalence relation \cong is a time-abstract bisimulation:
 - Action transitions: if $\nu \cong \mu$ and $\langle I, \nu \rangle \xrightarrow{a} \langle I', \nu' \rangle$ for some I', ν' , then there exists μ' s.t. $\nu' \cong \mu'$ and $\langle I, \mu \rangle \xrightarrow{a} \langle I', \mu' \rangle$
 - Wait transitions: if $\nu \cong \mu$, then for every $\delta \in \mathbb{Q}^+$ there exists $\delta' \in \mathbb{Q}^+$ s.t. $\nu + \delta \cong \mu + \delta'$
- \implies If $\nu\cong\mu$, then $\langle I,\nu\rangle$ and $\langle I,\mu\rangle$ satisfy the same temporal-logic formulas

Time-abstract Bisimulation in Regions



Number of Clock Regions

- Clock region: equivalence class of clock interpretations
- Number of clock regions upper-bounded by

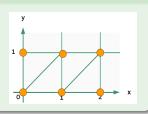
$$k! \cdot 2^k \cdot \prod_{x \in X} (2 \cdot C_x + 2), \quad s.t. \ k \stackrel{\text{def}}{=} ||X||$$

- finite!
- exponential in the number of clocks
- grows with the values of C_x

Example

- 2 clocks x,y, $C_x = 2$, $C_v = 1$
 - 8 open regions
 - 14 open line segments
 - 6 corner points
 - ⇒ 28 regions

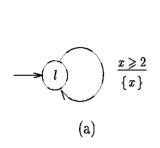
$$< 2 \cdot 2^2 \cdot (2 \cdot 2 + 2) \cdot (2 \cdot 1 + 2) = 192$$

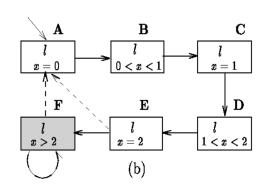


Region automaton

- Equivalent states = identical location + ≅-equivalent evaluations
- Equivalent Classes (regions): finite, stable, L^F-sensitive
- R(A): Region automaton of A
 - States: $\langle I, r(A) \rangle$ s.t. r(A) regions of A
 - ⇒ Finite state automaton!
- Reachability problem $\langle A, L^F \rangle \Longrightarrow$ Reachability problem $\langle R(A), L^F \rangle$
- ⇒ Reachability in timed automata reduced to that in finite automata!

Example: Region graph of a simple timed automata





May be further reduced (e.g., collapsing B, C, D into one state)

Complexity of Reasoning with Timed Automata

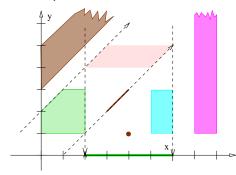
Reachability in Timed Automata

- Decidable!
- Linear with number of locations
- Exponential in the number of clocks
- Grows with the values of C_x
- Overall, PSPACE-Complete

Language-containment with Timed Automata Undecidable!

Zone Automata

- Collapse regions by convex unions of clock regions
- Clock Zone φ : set/conjunction of clock constraints in the form $(x_i \bowtie c), (x_i x_i \bowtie c), \bowtie \in \{>, <, =, \geq, \leq\}, c \in \mathbb{Z}$
- ullet φ is a convex set in the k-dimensional euclidean space
 - possibly unbounded
- → Contains all possible relationship for all clock value in a set
- Symbolic state: $\langle I, \varphi \rangle$
 - I: location
 - φ : clock zone



Zone Automata

Definition: Zone Automaton

- Given a Timed Automaton $A \stackrel{\text{def}}{=} \langle L, L^0, \Sigma, X, \Phi(X), E \rangle$,
- the Zone Automaton Z(A) is a transition system $\langle Q, Q^0, \Sigma, \rightarrow \rangle$ s.t.
 - Q: set of all zones of A (a zone is $\langle I, \varphi \rangle$)

 - Σ: set of labels/events in A
 - \rightarrow : set of "wait&switch" symbolic transitions, in the form: $\langle I, \varphi \rangle \stackrel{a}{\longrightarrow} \langle I', succ(\varphi, e) \rangle$
 - $\mathit{succ}(\varphi, e)$: successor of φ after (waiting and) executing the switch e
- $succ(\langle I, \varphi \rangle, e) \stackrel{\text{def}}{=} \langle I', succ(\varphi, e) \rangle$

Zone Automata: Symbolic Transitions

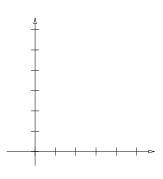
Definition: $succ(\varphi, e)$

- Let $e \stackrel{\text{def}}{=} \langle I, a, \psi, \lambda, I' \rangle$, and ϕ, ϕ' the invariants in I, I'
- Then

$$succ(\varphi, e) \stackrel{\text{def}}{=} (((\varphi \land \phi) \Uparrow \land \phi) \land \psi)[\lambda := 0]$$

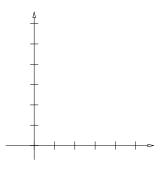
- A: standard conjunction/intersection
- \uparrow : projection to infinity: $\psi \uparrow \stackrel{\text{def}}{=} \{ \nu + \delta \mid \nu \in \psi, \delta \in [0, +\infty) \}$
- $[\lambda := 0]$: reset projection: $\psi[\lambda := 0] \stackrel{\text{def}}{=} \{ \nu[\lambda := 0] \mid \nu \in \psi \}$
- note: φ is considered "immediately before entering I"

Zone Automata: Symbolic Transitions (cont.)





Example: Zone Automata, Symbolic Transitions





Remark on $succ(\varphi, e)$

• In the above definition of $succ(\varphi, e)$, φ is considered "immediately before entering I":

$$succ(\varphi, e) \stackrel{\text{def}}{=} (((\varphi \land \phi) \uparrow \land \phi) \land \psi)[\lambda := 0]$$

• Alternative definition of $succ(\varphi, e)$, φ is considered "immediately after entering I":

$$succ(\varphi, e) \stackrel{\text{def}}{=} (((\varphi \uparrow \land \phi) \land \psi)[\lambda := 0] \land \phi')$$

- no initial intersection with the invariant ϕ of source location I (here φ is assumed to be already the result of such intersection)
- final intersection with the invariant ϕ' of target location I'

Symbolic Reachability Analysis

```
1: function Reachable (A, L^F) // A \stackrel{\text{def}}{=} \langle L, L^0, \Sigma, X, \Phi(X), E \rangle
 2: Reachable = \emptyset
 3: Frontier = \{\langle I_i, \{X=0\}\rangle \mid I_i \in L^0\}
 4: while (Frontier \neq \emptyset) do
            extract \langle I, \varphi \rangle from Frontier
 5:
            if (I \in L^F \text{ and } \varphi \neq \bot) then
 6:
                   return True
 7:
 8:
            end if
            if ( \not\exists \langle I, \varphi' \rangle \in Reachable s.t. \varphi \subseteq \varphi') then
 9:
                   add \langle I, \varphi \rangle to Reachable
10:
                   for e \in outcoming(I) do
11:
                           add succ(\varphi, e) to Frontier
12:
                   end for
13.
14:
            end if
15: end while
16: return False
```

Canonical Data-structures for Zones: DBMs

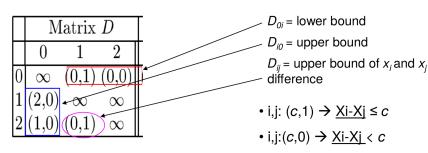
Difference-bound Matrices (DBMs)

- Matrix representation of constraints
 - bounds on a single clock
 - differences between 2 clocks
- Reduced form computed by all-pairs shortest path algorithm (e.g. Floyd-Warshall)
- Reduced DBM is canonical: equivalent sets of constraints produce the same reduced DBM
- Operations s.a reset, time-successor, inclusion, intersection are efficient
- → Popular choice in timed-automata-based tools

Difference-bound matrices, DBMs

- DBM: matrix $(k + 1) \times (k + 1)$, k being the number of clocks
 - added an implicit fake variable $x_0 \stackrel{\text{def}}{=} 0$ s.t. $x_i \bowtie c \Longrightarrow x_i x_0 \bowtie c$
 - each element is a pair (value, $\{0,1\}$), s.t " $\{0,1\}$ " means " $\{<,\leq\}$ "
- Example:

$$\begin{array}{lll} (0 \leq \dot{x}_1) & \wedge (0 < x_2) & \wedge (x_1 < 2) & \wedge (x_2 < 1) & \wedge (x_1 - x_2 \geq 0) \\ (x_0 - x_1 \leq 0) & \wedge (x_0 - x_2 < 0) & \wedge (x_1 - x_0 < 2) & \wedge (x_2 - x_0 < 1) & \wedge (x_2 - x_1 \leq 0) \end{array}$$



• i,j: ∞ \rightarrow absence of bound

Difference-bound matrices, DBMs (cont.)

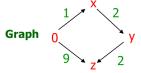
- Use all-pairs shortest paths, check DBM
 - idea: given $x_i x_j \bowtie c$, $x_i x_k \bowtie c_1$ and $x_k x_j \bowtie c_2$ s.t. $\bowtie \in \{\leq, <\}$, then c is updated with $c_1 + c_2$ if $c_1 + c_2 < c$
 - Satisfiable (no negative loops) ⇒ a non-empty clock zone
 - Canonical: Matrices with tightest possible constraints
- Canonical DBMs represent clock zones:
 equivalent sets of constraints ame reduced DBM

	Matrix D			Matrix D'		
	0	1	2	0	1	2
0	∞	(0,1)	(0,0)	(0,1) $(2,0)$ $(1,0)$	(0,1)	(0,0)
1	(2,0)	∞	∞	(2,0)	(0,1)	(2,0)
	(1,0)	(0,1)	∞	(1,0)	(0,1)	(0,1)

Canonical Data-structures for Zones: DBMs

When are two sets of constraints equivalent?

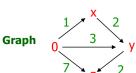
D1 | x<=1 y-x<=2 z-y<=2 z<=9



Shortest Path Closure



X<=1 y-x<=2 y<=3 z-y<=2 z<=7



Shortest Path Closure



Complexity Issues

- In theory:
 - Zone automaton might be exponentially bigger than the region automaton
- In practice:
 - Fewer reachable vertices ⇒ performances much improved

Timed Automata: summary

- Only continuous variables are timers
- Invariants and Guards: $x \bowtie const$, $\bowtie \in \{<,>,\leq,\geq\}$
- Actions: x:=0
- Reachability is decidable
- Clustering of regions into zones desirable in practice
- Tools: Uppaal, Kronos, RED ...
- Symbolic representation: matrices

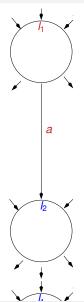
Decidable Problems with Timed Automata

- Model checking branching-time properties of timed automata
- Reachability in rectangular automata
- Timed bisimilarity: are two given timed automata bisimilar?
- Optimization: Compute shortest paths (e.g. minimum time reachability) in timed automata with costs on locations and edges
- Controller synthesis: Computing winning strategies in timed automata with controllable and uncontrollable transitions

Hybrid Automata



Hybrid Automata



Hybrid Automata $A = \langle L, L^0, X, \Sigma, \Phi(X), E \rangle$

- L: Set of locations.
- $L^0 \in L$: Set of initial locations
- X: Set of k continuous variables
- $\Phi(X)$: Set of Constraints on X
- Σ: Set of synchronization labels (alphabet)
- E: Set of edges
- State space: $L \times \mathbb{R}^k$,
 - state: $\langle I, \psi \rangle$ s.t. $I \in L$ and $\psi \in \mathbb{R}^k$
 - region ψ : subset of \mathbb{R}^k
- For each location /:
 - Initial states: region Init_i(X)
 - Invariant: region Inv_I(X)
 - Continuous dynamics: $\frac{dX}{dt} \in flow_l(X)$
- For each edge e from location I to location I'
 - Guard: region g(X) > 0
 - Update relation "Jump" J(X, X') over $\mathbb{R}^k \times \mathbb{R}^k$
 - Synchronization label $a \in \Sigma$ (communication information)

Remark: Degree of $flow_l(X)$

- Continuous dynamics described w.l.o.g. with sets of degree-1 differential (in)equalities flow_I(X)
- Sets/conjunctions of higher-degree differential (in)equalities can be reduced to degree 1 by renaming
- Ex:

$$(a_1 \frac{d^2s}{dt^2} + a_2 \frac{ds}{dt} + a_3 s + a_4 \bowtie 0)$$

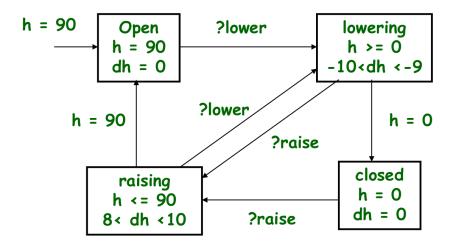
$$\downarrow \downarrow$$

$$(v = \frac{ds}{dt}) \wedge (a_1 \frac{dv}{dt} + a_2 v + a_3 s + a_4 \bowtie 0)$$

(Finite) Executions of Hybrid Automata

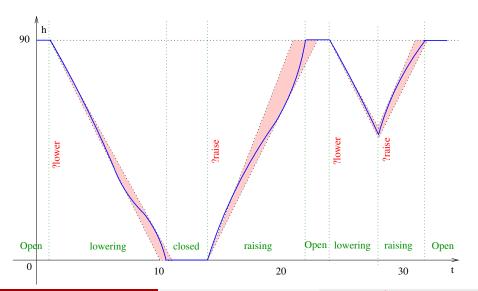
- State: pair $\langle I, X \rangle$ such that $X \in Inv_I(X)$
- Initialization: $\langle I, X \rangle$ such that $X \in Init_I(X)$
- Two types of state updates (transitions)
 - Discrete switches: $\langle I, X \rangle \xrightarrow{a} \langle I', X' \rangle$ if there there is an *a*-labeled edge *e* from *I* to *I'* s.t.
 - X, X' satisfy Inv_I(X) and Inv_{I'}(X) respectively
 - X satisfies the guard of e (i.e. $g(X) \ge 0$) and
 - $\langle X, X' \rangle$ satisfies the jump condition of e (i.e., $\langle X, X' \rangle \in J(X, X')$)
 - Continuous flows: $\langle I, X \rangle \xrightarrow{f} \langle I, X' \rangle$ f is a continuous function in $[0, \delta]$ s.t.
 - f(0) = X
 - $f(\delta) = X'$
 - for every $t \in [0, \delta]$, $f(t) \in Inv_l(X)$
 - for every $t \in [0, \delta]$, $\frac{d\hat{f}(t)}{dt} \in flow_l(X)$

Example: Gate for a railroad controller



Notation: "dh" shortcut for " $\frac{dh}{dt}$ "

Example: Gate for a railroad controller



General Symbolic-Reachability Schema

```
1: R = I(X)
 2: while (True) do
      if (R intersects F) then
 3:
         return True
 4.
      else
 5:
         if (Image(R) \subseteq R) then
 6:
           return False
 7:
        else
 8.
           R = R \cup Image(R)
 9:
         end if
10:
      end if
11:
12: end while
```

- I: initial; F: Final; R: Reachable; Image(R): successors of R
- need a data type to represent state sets (regions)
- Termination may or may not be guaranteed

Symbolic Representations

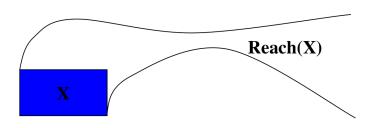
- Necessary operations on Regions
 - Union
 - Intersection
 - Negation
 - Projection
 - Renaming
 - Equality/containment test
 - Emptiness test
- Different choices for different classes of problems
 - BDDs for Boolean variables in hardware verification
 - DBMs in Timed automata
 - Polyhedra in Linear Hybrid Automata
 - ...

Reachability for Hybrid Systems

- Same algorithm works in principle
- Problem: What is a suitable representation of regions?
 - Region: subset of \mathbb{R}^k
 - Main problem: handling continuous dynamics
- Precise solutions available for restricted continuous dynamics
 - Timed automata
 - Multi-rate & Rectangular Hybrid Automata (reduced to Timed aut.)
 - Linear Hybrid Automata
- Even for linear systems, over-approximations of reachable set needed

Reachability Analysis for Dynamical Systems

- Goal: Given an initial region, compute whether a bad state can be reached
- Key step: compute Reach(X) for a given set X under $\frac{dX}{dt} = f(X)$



Notation: (hereafter we often use "dX" or " \dot{X} " as a shortcut of " $\frac{dX}{dt}$ "

Simple Hybrid Automata: Multi-Rate and Rectangular

Two simple forms of Hybrid Automata

- Multi-Rate Automata
- Rectangular Automata
- Idea: can be reduced to Timed Automata
- typically used as over-approximations of complex hybrid automata

Multi-rate Automata

- Modest extension of timed automata.
 - Dynamics of the form $\frac{dX}{dt} = const$ s.t. the rate of of each variable is the same in all locations
 - Guards and invariants: x < const, x > const
 - Resets: x := const
- Simple translation to timed automata by shifting and scaling:
 - if $x_i := d_i$ then rename it with a fresh var v_i s.t. $v_i + d_i = x_i$
 - if $\frac{dx_i}{dt} = c_i$, then rename it with a fresh var u_i s.t. $c_i \cdot u_i = x_i$
 - shift & rescale constants in constraints accordingly

$$x>5$$
 and $y<1$

$$dx = 2$$

$$dy = 3$$

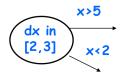
$$u>5/2 \text{ and } v<1/3$$

$$du = 1$$

$$dv = 1$$

Rectangular Automata (simplified)

- More interesting extension of timed automata
 - Dynamics of the form $\frac{dX}{dt} \in [const1, const2]$ s.t. the rate of each variable is the same in all locations
 - Guards and invariants: x < const, x > const
 - Jumps: x := const
- Translation to multi-rate automata (hints). For each x:
 - Introduce x_M , x_m describing the greatest/least possible x values
 - flow: substitute $\dot{x} < c_u$ with $\dot{x}_M = c_u$ and $\dot{x} > c_l$ with $\dot{x}_m = c_l$
 - invariants: substitute $Inv_I(x)$ with $Inv_I(x_M)$, $Inv_I(x_m)$
 - guards: substitute x > c with $x_M > c$, add jump $x_m := c$ (if none) substitute x < c with $x_m < c$, add jump $x_M := c$ (if none)
 - jump: if x := c, then both $x_M := c$ and $x_m := c$



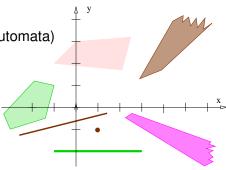


$$x_{M} > 5, x_{m} := 5$$
 $dx_{M} = 2$
 $dx_{M} = 3$
 $x_{M} < 2, x_{M} := 2$

Linear Hybrid Automata

- Polyhedron φ : set/conjunction of linear inequalities on X in the form $(A \cdot X \geq B)$, s.t. $A \in \mathbb{R}^m \times \mathbb{R}^k$ and $B \in \mathbb{R}^m$ for some m.
- $\bullet \ \varphi$ is a convex set in the k-dimensional euclidean space
 - possibly unbounded
- → Contains all possible values for all variables in a set
 - Symbolic state: $\langle I, \varphi \rangle$
 - I: location
 - φ : polyhedron

(generalization of zone automata)



Linear Hybrid Automata $A = \langle L, L^0, X, \Sigma, \Phi(X), E \rangle$

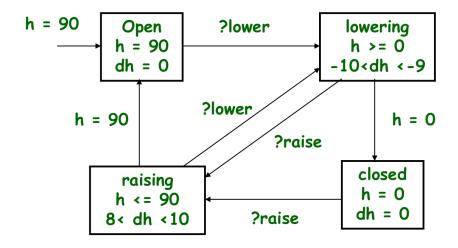
- State space: $L \times \mathbb{R}^k$,
 - state: $\langle I, \psi \rangle$ s.t. $I \in L$ and $\psi \in \mathbb{R}^k$
 - polyhedron ψ : subset of \mathbb{R}^k in the form $A \cdot X > B$
- For each edge e from location I to location I'
 - Guard: region $(A \cdot X \geq B)$: polyhedron on X
 - Update relation "Jump" J(X, X'): $X' := T \cdot X$, $T \in \mathbb{R}^k \times \mathbb{R}^k$
 - Synchronization label $a \in \Sigma$ (communication information)
- For each location I:
 - Initial states: region Init_I(X): polyhedron on X
 - Invariant: region Inv(X): polyhedron on X
 - Continuous dynamics $flow_l(X)$: polyhedron on $\frac{dX}{dt}$

Continuous Dynamics

Time-invariant, state-independent dynamics specified by a convex polyhedron constraining first derivatives

Es:
$$\frac{dx}{dt} \ge 3$$
, $\frac{dx}{dt} = \frac{dy}{dt}$, $2.1 \frac{dx}{dt} - 3.5 \frac{dy}{dt} + 1.7 \frac{dz}{dt} \ge 3.1$, ...

Example: Gate for a railroad controller



Reachability Computation: Key Steps

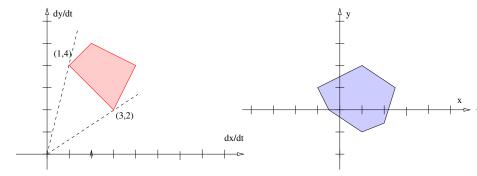
- Compute "discrete" successors of $\langle I, \psi \rangle$
- Compute "continuous" successor of $\langle I, \psi \rangle$
- Check if ψ intersects with "bad" region
- Check if newly found ψ is covered by already visited polyhedra $\psi_1,...,\psi_n$ (expensive!)

Computing Discrete Successors of $\langle I, \psi \rangle$

- Intersect ψ with the guard ϕ \Longrightarrow result is a polyhedron
- Apply linear transformation of J to the result
 result is a polyhedron
- Intersect with the invariant of target location I'
 ⇒ result is a polyhedron

Computing Time Successor

- Consider maximum and minimum rates between derivatives (external vertices in the flow polyhedron)
- Apply these extremal rates for computing the projection to infinity (to be intersected with invariant)
 - Hint: $\frac{dx}{dy} = \frac{\frac{dx}{dt}}{\frac{dy}{dy}}$, s.t. $\max_{x,y} \frac{dx}{dy} = \max_{x,y} \frac{\frac{dx}{dt}}{\frac{dy}{dt}}$ and $\min_{x,y} \frac{dx}{dy} = \min_{x,y} \frac{\frac{dx}{dt}}{\frac{dy}{dt}}$



Linear Hybrid Automata: Symbolic Transitions

Definition: $succ(\varphi, e)$

- Let $e \stackrel{\text{def}}{=} \langle I, a, \psi, J, I' \rangle$, and ϕ, ϕ' the invariants in I, I'
- Then

$$succ(\varphi, e) \stackrel{\text{def}}{=} J(((\varphi \land \phi) \uparrow \land \phi) \land \psi)$$

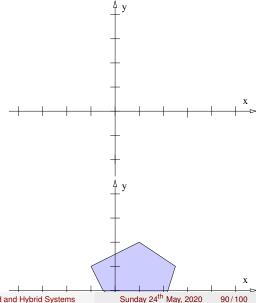
(φ immediately before entering the location)

$$succ(\varphi, e) \stackrel{\text{def}}{=} J((\varphi \uparrow \land \phi) \land \psi) \land \phi'$$

(φ immediately after entering the location):

- A: standard conjunction/intersection
- \uparrow : continuous successor $\psi \uparrow$
- J: Jump transformation $J(X) \stackrel{\text{def}}{=} T \cdot X$
- note: φ is considered "immediately after entering l"

Linear Hybrid Automata: Symbolic Transitions (cont.)



Symbolic Reachability Analysis

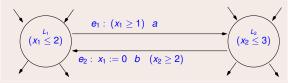
```
1: function Reachable (A, F) // A \stackrel{\text{def}}{=} \langle L, L^0, \Sigma, X, \Phi(X), E \rangle, F \stackrel{\text{def}}{=} \{ \langle I_i, \phi_i \rangle \}_i
 2: Reachable = \emptyset
 3: Frontier = \{\langle I, Init_I(X) \rangle \mid I \in L^0\}
 4: while (Frontier \neq \emptyset) do
             extract \langle I, \varphi \rangle from Frontier
 5:
             if ((\varphi \land \phi) \neq \bot for some \langle I, \phi \rangle \in F) then
 6:
                     return True
 7:
             end if
 8:
             if ( \not\exists \langle I, \varphi' \rangle \in Reachable s.t. \varphi \subseteq \varphi') then
 9:
                     add \langle I, \varphi \rangle to Reachable
10:
                     for e \in outcoming(I) do
11:
                             add succ(\varphi, e) to Frontier
12:
                     end for
13:
14:
             end if
15: end while
16: return False
```

Summary: Linear Hybrid Automata

- Strategy implemented in HyTech
- Core computation: manipulation of polyhedra
- Bottlenecks
 - proliferation of polyhedra (unions)
 - computing with high-dimension polyhedra
- Many case studies

Ex: Execution of a Timed System

Consider only the following piece of a timed automaton A, x_1 and x_2 being clocks.



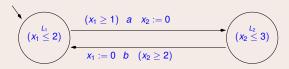
- (a) In general, what is the minimum amount of time from an occurrence of event b and the subsequent occurrence of the event a? [Solution: 1 time unit.]
- (b) Write a legal execution from state $\langle L_1, 0.0, 2.0 \rangle$ to state $\langle L_1, 0.0, 3.0 \rangle$. [Solution: $\langle L_1, 0.0, 2.0 \rangle \xrightarrow{1.0} \langle L_1, 1.0, 3.0 \rangle \xrightarrow{a} \langle L_2, 1.0, 3.0 \rangle \xrightarrow{0.0} \langle L_2, 1.0, 3.0 \rangle \xrightarrow{b} \langle L_1, 0.0, 3.0 \rangle$]
- (c) Is it possible to have a legal execution in which switches e₂, e₁, e₂ are shot consecutively (possibly interleaved by time elapses), without being interleaved by other switches? If yes, write one such execution. If not, explain why. [Solution:

Yes:
$$\langle L_2, ..., 2.0 \rangle \xrightarrow{b} \langle L_1, 0.0, 2.0 \rangle \xrightarrow{1.0} \langle L_1, 1.0, 3.0 \rangle \xrightarrow{a} \langle L_2, 1.0, 3.0 \rangle \xrightarrow{0.0} \langle L_2, 1.0, 3.0 \rangle \xrightarrow{b} \langle L_1, 0.0, 3.0 \rangle$$

Note: if the guard of e_2 were strictly greater than 2, this would not be possible.]

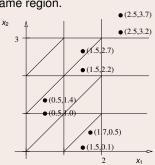
Ex: Timed Automata: Regions

Consider the following timed automaton A.



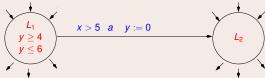
Considere the correponding Region automaton R(A). For each of the following pairs of states of A, say if the two states belong to the same region.

- (a) $s_0 = (L_1, 2.5, 3.2), s_1 = (L_1, 2.5, 3.7)$ [Solution: yes]
- (b) $s_0 = (L_1, 1.5, 2.2), s_1 = (L_1, 1.5, 2.7)$ [Solution: no]
- (c) $s_0 = (L_2, 0.5, 1.4), s_1 = (L_2, 0.5, 1.0)$ [Solution: no]
- (d) $s_0 = (L_2, 1.7, 0.5), s_1 = (L_2, 1.5, 0.1)$ [Solution: yes]

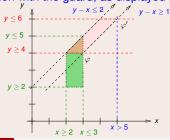


Ex: Timed Automata: Zones

Consider the following switch e in a timed automaton, x and y being clocks:



and let $Z_1 \stackrel{\text{def}}{=} \langle L_1, \varphi \rangle$ s.t $\varphi \stackrel{\text{def}}{=} (x \ge 2) \land (x \le 3) \land (y \ge 2) \land (y \le 5) \land (y - x \le 2)$. Compute $succ(Z_1, e)$, drawing the process on the cartesian space $\langle x, y \rangle$. [Solution: The solution is $succ(Z_1, e) = \langle Z_2, \bot \rangle$. In fact, the zone reached by waiting in L_1 has empty intersection with the guard, as displayed in figure:

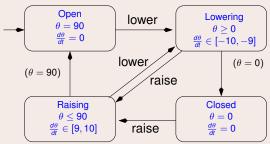


Hybrid Automata

A railway-crossing gate, whose dynamics is represented by the hybrid automaton in the figure, receives from a controller two possible input signals {lower,raise}. (θ , in degrees, represents the angle between the bar and the ground.)

When the gate is open the controller receives a signal "incoming" when a train is incoming, it waits a fixed amount of time Δt , then it sends the gate the lower order. It is known that an incoming train takes an amount of time within the interval [70,100] time units to get from the remote sensor to the gate.

Compute the maximum amount of time Δt which guarantees that the train does not reach the gate before the bar is completely lowered, and briefly explain why.



Hybrid Automata

[Solution: Δt is 60 time units. In fact, the maximum value of Δt the controller can afford waiting is given by the minimum time the train may take to reach the gate (70), minus the maximum time taken by the bar to lower, that is, the time taken to lower the angle from 90 to 0 at the lowest absolute speed (90/|-9|). Overall, we have thus $\Delta t = 70 - 90/(|-9|) = 60.1$

Difference Bound Matrices

Consider the zone:

$$\varphi \stackrel{\text{def}}{=} (x_1 \le 3) \land (x_2 \le 2) \land (x_3 \le 5) \land (x_1 - x_3 \le 2) \land (x_2 - x_1 \le -2) \land (x_3 - x_1 \le 3) \land (x_3 - x_2 \le 1)$$

- (a) Compute the corresponding DBM
- (b) Compute the reduced DBM

Difference Bound Matrices

[Solution:
$$\varphi \stackrel{\text{def}}{=} (x_1 \le 3) \land (x_2 \le 2) \land (x_3 \le 5) \land (x_1 - x_3 \le 2) \land (x_2 - x_1 \le -2) \land (x_3 - x_1 \le 3) \land (x_3 - x_2 \le 1)$$

Initial DBM: Reduced DBM:

X_0 X_2 X_3 X_0 X_2 *X*₃ $\langle \infty, \leq \rangle$ $\langle \infty, < \rangle$ $\langle \infty, < \rangle$ $\langle \infty, \leq \rangle$ $\langle 0, \leq \rangle$ $\langle \infty, \leq \rangle$ $\langle \infty, \leq \rangle$ $\langle \infty, \leq \rangle$ X_0 *X*₀ $\langle 2, \leq \rangle$ $\langle 3, \leq \rangle$ $\langle \infty, \leq \rangle$ $\langle \infty, \leq \rangle$ $\langle 3, \leq \rangle$ $\langle 0, \leq \rangle$ $\langle 3, \leq \rangle$ $\langle 2, \leq \rangle$ X₁ *X*₁ $\langle -2, \leq \rangle \quad \langle \infty, \leq \rangle$ $\langle \infty, < \rangle$ $\langle 1, \leq \rangle \quad \langle -2, \leq \rangle \quad \langle 0, \leq \rangle$ $\langle 2, < \rangle$ *X*₂ X_2 $\langle 5, \leq \rangle$ $\langle 3, \leq \rangle$ $\langle 1, \leq \rangle$ $\langle \infty, \leq \rangle$ X_3

