
Curriculum Vitae

Prof. Ing. **Roberto Sebastiani**, PhD

(last update: September 17, 2019)



Brief Resume

General Information

Born on 31.03.1966. Italian Citizen. Married, with two children. Living in Povo, Trento, Italy.

Mother tongue: Italian. Fluent in English.

Phone: +39.0461.281514; Email: roberto.sebastiani@unitn.it. URL: disi.unitn.it/rseba/

Current Status

- Qualified as Full Professor (Professore Abilitato di I Fascia) on both sectors 01/B1 [29.01.2014 → 29.01.2020; 11/09/2019 → 11/09/2025] and 09/H1 [03.12.2013 → 03.12.2019; 09/09/2019 → 09/09/2025].
- Associate Professor (Professore di II Fascia), sector 09/H1 (previously ING-INF/05), at *DISI – Dipartimento di Ingegneria e Scienza dell’Informazione, Università degli Studi di Trento, Italy* (Dept. of Information Science and Engineering, University of Trento).

Career Summary

- (January 2014) Qualified as Full Professor (Abilitazione Nazionale a Professore di I Fascia) sector 01/B1.
- (December 2013) Qualified as Full Professor (Abilitazione Nazionale a Professore di I Fascia) sector 09/H1.
- (January 2005-current.) Associate Professor at *Università degli Studi di Trento*, Dipartimento di Ingegneria e Scienza dell’Informazione (DISI), Facoltà di Scienze M.F.N..
- (January 2001-Dec. 2004.) Assistant Professor at *Università degli Studi di Trento*, Facoltà di Scienze M.F.N..
- (February 1999-Dec. 2000.) Research associate (“Ricercatore a tempo determinato”) at *Dipartimento di Informatica e Sistemi Aziendali, DISA, Università degli Studi di Trento*.
- (July 1997-January 1999.) Researcher at *Istituto per la Ricerca Scientifica e Tecnologica – ITC-Irst* (now FBK-Irst), Trento.
- (April-June 1994 and May-June 1996) Visiting Scholar at Dept of Computer Science, Stanford University, CA, USA.
- (November 1993-June 1997.) PhD student at Dipartimento di Informatica, Sistemistica e Telematica (DIST), Università degli Studi di Genova.

Education

- (September 1997) **PhD in Computer-Science Engineering.** *Dipartimento di Informatica, Sistemistica e Telematica, Università degli Studi di Genova.*
Thesis: “Una nuova classe di procedure di decisione per logiche modali e terminologiche: teoria, implementazione e testing”¹ Advisors: Mauro Di Manzo (DIST, Genova), Fausto Giunchiglia (IRST Trento).
- (December 1991) **M.S. in Electronic Engineering,** 110/110 cum Laude. *Università degli Studi di Padova.*
Thesis: “L’astrazione in un dimostratore automatico interattivo: definizione e realizzazione.” Advisors: Enrico Pagello (University of Padua), Fausto Giunchiglia (IRST Trento).
- (June 1985) **Diploma di Maturità Scientifica.** 60/60. *Liceo Scientifico “G.Galilei”, Trento.*

¹Before 1998, by Italian Law, both PhD and Master Theses had mandatorily to be written in Italian.

Research

Research Interests

His current research activities deal with the following topics:

Satisfiability Modulo Theories (SMT) and its applications. SMT tools are nowadays widely used in industries as horsepower reasoning engines in the verification of both SW and HW systems.

He is one of the inventors of the “lazy” approach to SMT (now implemented in most state-of-the-art SMT tools), which combines SAT solvers with decision procedures for the conjunctive fragment of the theories.²

Since 2002 he has been giving essential contributions, from both theoretical and empirical viewpoint, in SMT solving and in advanced SMT techniques, like theory combination, interpolation, unsatisfiable-core extraction. All these techniques have been implemented into the state-of-the-art SMT tool MATHSAT.

Optimization Modulo Theories (OMT) and its applications. Recently he has initiated and is pushing forward the field of Optimization Modulo Theories (OMT) as a very-useful extension of SMT –and viable alternative to MILP and Constraint Programming– in many application domains. All the OMT techniques developed have been implemented into the state-of-the-art OMT tool OPTIMATHSAT.

Formal Verification of software and hardware. His main research contributions in formal verifications are divided into two main streams. (See also “Technology transfer to industry”.)

First, he has contributed to the research on model checking (MC), by investigating novel LTL model checking algorithms, both theoretically and empirically.

Second, he is one of the main contributors of SMT-based model checking, which leverages previous SAT-based MC to much more expressive application domains –e.g., real-time & hybrid systems, RTL designs and microcode, software– by exploiting SMT technology.

Currently his main interests are on the application of Optimization Modulo Theories (OMT) to the verification of systems (parametric systems, software).

Modeling and Formal Reasoning with Software Requirements Goal models have been widely used in Computer Science to represent software requirements, business objectives, and design quality. Since 2002 he has been working on formalizing Goal Models and on applying automated reasoning techniques (SAT, and recently OMT) so that to provide tools for modeling and reasoning with goal models within the TROPOS methodology. All these techniques have been implemented into GR-TOOL, a tool for requirement analysis which has been used in many projects.

Recently his research focuses on modeling and reasoning with *constrained goal models*, which extend goal models with SMT constraints, so that to (i) allow to associate resources and quantitative attributes to goals, tasks, requirements and assumptions so that to find optimal realizations, and to (ii) allow for representing and reasoning on the evolution of goal models, minimizing the effort of change. In both cases the OMT tool OPTIMATHSAT is used as automated-reasoning backend.

Automated Reasoning on Modal & Description Logics. He has given important contributions to automated reasoning on Modal & Description Logics, by introducing and exploiting SAT-based reasoning techniques there. Lately he has been interested in the problem of debugging ontologies represented by \mathcal{EL}^+ logic.

Quantum Computing. He is currently collaborating with D-Wave System Inc. on solving NP-Hard problems via Quantum Annealing via encoding into Ising Models. Some results have already been published.

Probabilistic Reasoning via Weighted Model Integration. Recently he has given important contributions on SMT-based Weighted Model Integration, a very-effective framework for probabilistic reasoning.

His past research activities dealt also with the following topics:

SAT. He has given some contributions on SAT solving for non-CNF formulas.

Planning. He has given some contributions to abstraction-based planning and SAT-based planning.

Abstraction-based theorem proving. He has given some contributions to abstraction-based theorem proving.

²This fact is witnessed, e.g., by the explicit statement in the preface of the Special Issue “Satisfiability Modulo theories” by O. Strichman and D. Kroening, Formal Methods in System Designs 42:12, 2013. <http://dl.acm.org/citation.cfm?id=2429205>.

Development of tools for Automated Reasoning, Formal Verification and Requirement Engineering

The following is a list of tools for Automated Reasoning, Formal Verification and Requirement Engineering whose development I've been involved with.

- (2018-current) I co-lead the development of PYWMI, a tool for weighted model integration for probabilistic reasoning.
(<https://pypi.org/project/pywmi/>).
- (2017-current) I co-lead the development of WMI_PA, a tool for weighted model integration for probabilistic reasoning.
(<https://github.com/unitn-sml/wmi-pa/>).
- (2014-current). I co-lead the development of CGMTOOL, a tool for modeling and reasoning on *constrained goal models* for requirement engineering
(<http://www.cgm-tool.eu/>).
- (2011-current). I lead the development of OPTIMATHSAT, a tool for *optimization modulo theories* built on top of the SMT solver MATHSAT
(<http://optimathsat.disi.unitn.it/>).
- (2010-current). I lead the development of EL+SAT, a SAT/SMT-based tool for debugging medical ontologies via axiom pinpointing in the description logic \mathcal{EL}^+
(<http://disi.unitn.it/rseba/elsat/>).
- (2001-current). I co-lead the development of MATHSAT, a state-of-the-art SMT solver
(<http://mathsat.fbk.eu>).
- (2000-2011) I have participated to the development of the Model Checker NuSMV.2
(<http://nusmv.fbk.eu>).
- (2002-2005) I have participated to the development of the tool GR-TOOL for automated reasoning in goal models for requirement engineering
(<http://troposproject.org/tools/grtool/>).

The following is a list of tools for Automated Reasoning and Formal Verification, whose development I was involved with in the past, which are no more available or maintained.

- (2003) I led the development of the tool MODELLEA for LTL model checking.
- (1999-2004) I led the development of a tool for automated career planing and verification of regulations and ordinances, based on a Model Checker.
- (1997-1998) I participated to the development of the decision procedure TABLEAU* for SAT-encoded planning (interfaced with the planner MEDIC).
- (1995-1998) I developed the theorem prover KSAT for modal & description logics $K(m)/ALC$.
- (1993) I developed NC_GSAT, a prototype SAT tool extending of GSAT to non-CNF Boolean formulas.
- (1990-1992) I participated to the development of GETFOL, an interactive theorem prover for first-order logic.

Technology Transfer and External Fundings

Technology transfer to industry

Tech-transfer projects with D-Wave Systems Inc. Since 2015 he has been collaborating with D-Wave Systems Inc. at Burnaby (CA), “The First Quantum Computer Company” (<https://www.dwavesys.com>) on solving SAT and other NP-complete problems with D-Wave’s Quantum Annealers (QAs).

This work, which is currently ongoing and whose first results are presented in a joint publication [C6], aims at developing encoding techniques for D-Wave’s QA’s so that to effectively and efficiently encode and solve SAT problems and related NP-complete problems.

A D-Wave-sponsored project QuASi (Quantum Annealing for SAT Solving) has been signed, effective from 01-04-2018.

Tech-transfer projects with Intel Corporation. Since 2003, he has been collaborating with the Formal Verification Group of Intel at Haifa, Israel, on SMT-based Formal Verification of industrial RTL designs and microcode, in four projects which were fully or partly funded by Intel (namely BOWLING, ORCHID, WOLFLING, WOLF, see “Fundings”).

Remarkably, this work, which is described in five joint publications [C41, J16, C31, J14, C23], led to the development of a customized version of our SMT solver MATHSAT v.4, which was embedded as backend engine into the production version of Intel’s microcode symbolic simulator μ Formal, leading to an average speedup of one order magnitude in the verification processes [C23]. Since then this customized version of MATHSAT v.4 had been used for years by Intel, as backend engine of μ Formal, for verifying the microcode of its processors (in particular, the microcode for the Sandy Bridge architecture developed by Intel in Haifa).

The success of this work is witnessed by the following quote from the statement motivating the best-paper award of our joint paper at Formal Methods in Computer-Aided Design conference in 2010 [C23]:³

“The FMCAD10 best paper award goes to the paper “Applying SMT in Symbolic Execution of Microcode” by Anders Franzen, Alessandro Cimatti, Alexander Nadel, Roberto Sebastiani, and Jonathan Shalev. The paper is a pleasure to read, and provides impressive results. The paper shows that SMT-based techniques can beat SAT-techniques in an industrial setting, even for problems where SAT has previously been the dominating technique. These results, although anticipated by the SMT community for a long time, had not been established this conclusively so far. Bravo and congratulations!”

Tech-transfer projects with Ansaldo Segnalamento Ferroviario. From 1997 to 1998, when employed at IRST, Trento, he has very actively participated to the following technology transfer projects with Ansaldo Segnalamento Ferroviario for the usage of formal specification, design and verification methods for safety-critical railway signalling systems:

SCAPIII - Safety Critical Applications: formal specification, design and verification of a train-to-station communication protocol;

ETCS – European Train Control System: formal specification of a Train-Management system;

RBC – Radio Block Center: formal specification of a train-to-station transmission system.

These projects led to two joint publications [C55, C56].

³See <http://fmcad10.iaik.tugraz.at/>.

Fundings

- (2018-2021) Principal investigator of the 3-year project “*QuASi, Quantum Annealing for SAT Solving*” sponsored by D-Wave Systems Inc. [150K CA\$]
- (2012-2016) Principal investigator of the SRC-GRC 3-year project “*WOLF, Advanced SMT techniques for Word-level formal Verification*” sponsored by Semiconductor Research Corporation. [210K US\$]
- (2011-2017) Participation as Co-investigator to the project “*LUCRETIUS: Foundations for Software Evolution*”, ERC Advanced Grant # 267856
- (2009-2012) Principal investigator of the 3-year SRC-GRC project “*WOLFLING, Word-level formal verification via SMT solving*” sponsored by Intel Corporation via Semiconductor Research Corporation. [180K US\$]
- (2007-2009) Local team leader of the 3-year PRIN project “*Integrating automated reasoning in model checking: towards push-button formal verification of large-scale and infinite-state systems*” funded by MIUR. [24,5K EU]
- (2004-2006) Local team leader of the “Fondo unico” research project “*ORCHID: Enhanced Formal Checkers for RTL Circuit Designs*” sponsored by PAT in collaboration with Intel Corporation. [120K EU]
- (2003-2005) Local team leader of the technology transfer project “*BOWLING: BBoolean and Word-Level Integrated engines for Hybrid Formal Checking of RTL Designs*” sponsored by Intel Corporation. [37,5K US\$]
- (2003-2005) Participation to the research project “*Automazione dell’ Ingegneria del Software basata su Conoscenza – ASTRO*” sponsored by MIUR-FIRB.
- (2003-2004) Local team leader of the research project Partecipazione al progetto di ricerca “*Sistemi avanzati di ragionamento automatico per la rappresentazione e la verifica formale di sistemi complessi basati su estensioni non booleane di decisori per soddisfabilità*” sponsored by MURST.
- (2000-2004) Participation as external consultant of ITC-IRST to the EU IHP-RTN 5th Framework project “*CALCULEMUS*” on the integration of computer algebra and automated deduction systems.
- (2000-2001) Participation to the project “*Safety Critical Software for planning in space robotics*” sponsored by Agenzia Spaziale Italiana.
- (2000-2001) Participation to the project “*Model checking e satisfiability: sviluppo di nuove procedure di decisione, loro valutazione ed analisi sperimentale comparata in ambito applicativo*” sponsored by MURST.

Lecturing and Teaching

Invited Talks at conferences

- “SMT-Based Weighted Model Integration” at 17th International Workshop on Satisfiability Modulo Theories, Lisbon, PT, 2019
- “SMT: From Satisfiability to Optimization” at Alpine Verification Meeting (AVM13) workshop, 2013.
- “From SAT to Satisfiability Modulo Theories” at Guangzhou Symposium on Satisfiability in Logic-Based Modeling, 2010
- “From Description Logics to Satisfiability Modulo Theories (and back?)” at DL’10 Workshop (2010)
- “Lazy Satisfiability Modulo Theories” at DoD Workshop on Advances in Satisfiability (2008)
- “Delayed theory combination” at Dagstuhl Seminar on Decision Procedures (2007)
- “From KSAT to Delayed Theory Combination: Exploiting DPLL Outside the SAT Domain” at Frontier on Combining Systems (2007)
- “On Efficiently Integrating Boolean and Theory-Specific Solving Procedures” at STRATEGIES (2004)
- “Evaluating the Efficiency of Decision Procedures for Modal Logics” Methods for Modalities (1999)

Courses & Tutorials at International Schools and Conferences

- (2019. Macao, China) “From Satisfiability to Optimization Modulo Theories” at *International Joint Conference on Artificial Intelligence - IJCAI’19*.
- (2019. Lisbon, Portugal.) “Optimization Modulo Theories” at *International SAT/SMT/AR School*.
- (2015. Stanford, CA, USA) “Optimization Modulo Theories” at *International SAT/SMT School*.
- (2011. Barcellona, Spain) Tutorial “Satisfiability Modulo Theories” at *International Joint Conference on Artificial Intelligence - IJCAI’11*.
- (2008. Gargnano, Italy). “Logic at Work.” Summer School of Logic, Gargnano, Italy.
- (2005-2006. Brixen, Italy.) “Efficient Boolean Reasoning” at “Int. BIT summer school in ICT”.
- (2003, Acapulco, Mex) Tutorial “SAT beyond propositional satisfiability”, *International Joint Conference on Artificial Intelligence – IJCAI’03*
- (2003, Miami, USA) Tutorial “SAT beyond propositional satisfiability”, *International Conference on Automated Deduction CADE’03*
- (2002, Trento). Advanced Course “SAT beyond propositional satisfiability”, *14th European Summer School in Logic, Language and Information – ESSLLI’2002*.

PhD Courses

- (2017-18, 2015-16, 2013-14, 2011-12, 2009-10, 2007-8, 2005-6, 2004-5, 2003-4, 2002-3, 2001-2002.) [3CFU] PhD Course “Efficient Boolean Reasoning”, “Int. Graduate School on ICT”, Trento.

M.S. Courses (at c.d.l.m. “Informatica”, DISI, Università di Trento.)

- (2018-2019 \Leftarrow 2003-4, yearly) [12CFU] M.S. course “Introduction to Formal Methods”.

B.S. Courses (at c.d.l. “Informatica”, DISI, Università di Trento.)

- (2018-2019 \Leftarrow 2003-4, yearly) [12CFU] B.S. course “Programmazione 1” (“Programming, 1”).
- (2002-2003, 2001-2002) [6 CFU] B.S. course “Laboratorio di Informatica: Programmazione 1”,
- (2000-2001) [6 CFU] B.S. course “Laboratorio di Informatica: Programmazione 2”,

B.S. Courses (at c.d.l. “Scienze e Ingegneria dell’Informazione”, Free University of Bozen/Bolzano.)

- (2010-11) [8 CFU] B.S. course “Data Structures and Algorithms”.

Student Advising and Service Activities

Advisor of PhD students

He is (co-)supervising/he has (co-)supervised (see <http://disi.unitn.it/rseba/students.html>):

- (2016-current.). Co-advisor of Paolo Morettin. Topics in artificial intelligence.
- (2015-2019.). Advisor of Stefano Varotti. Topics in quantum computing.
- (2014-2019.). Advisor of Patrick Trentin. Topics in automated reasoning and optimization.
- (2012-2017.). Advisor of Mai Chi Nguyen. Topics in automated reasoning and requirement specification.
- (2012-2018.). Co-advisor of Ahmed Irfan. Topics in automated reasoning and formal verification.
- (2010-2013.). Advisor of Bastian Joost Schaafsma. Topics in automated reasoning and formal verification.
- (2009-2012.). Advisor of Jeremy Ridgeway. Topics in automated reasoning and formal verification.
- (2009-2013.). Advisor of Silvia Tomasi. Topics in automated reasoning and optimization.
- (2006-2011.). Advisor of Michele Vescovi. Topics in automated reasoning in description logics.
- (2005-2010.). Co-Advisor of Anders Franzen. Topics in automated reasoning and formal verification.
- (2005-2009.). Advisor of Alberto Griggio. Topics in automated reasoning and formal verification.
- (2004-2008.). Co-Advisor of Krishnamani Kalyanasundaram (Advisor: Alessandro Cimatti)
- (2004-2008). Co-Advisor of Roberto Bruttomesso. Topics in automated reasoning and formal verification.
- (2003-2005.). Co-Advisor of Veselin Kirov. Topics in automated reasoning and formal verification.
- (2001-2006.). Advisor of Stefano Tonetta. Topics in automated reasoning and formal verification.

Advisor of M.S. and B.S students

He is supervising/he has supervised (see <http://disi.unitn.it/rseba/students.html>):

- 23 M.S. theses in Computer Science,
- 1 M.S. thesis in Mathematics,
- 1 M.S. thesis in Economical Science,
- 20 B.S. theses in Computer Science.

Current or past Service Activities

- Manager of the c.d.l. “Informatica” at DISI
- Member of the restricted commission for the new M.S. course “Artificial Intelligence Systems”.
- Member of the commission “Accordi Bilaterali Mobilità di Ateneo”.
- Member of the commission “Esame di stato per l’abilitazione alla professione di Ingegnere”.
- Member for student-professor joint commission (membro “Commissione Paritetica Docenti-Studenti”)
- Delegate for student secondary-school stages (“alternanza scuola-lavoro”) at DISI, University of Trento.
- Delegate for student orientation and guidance (“delegato per l’orientamento”) at DISI, University of Trento.
- Member of the Executive Committee of the “Int. Graduate School on ICT” of University of Trento.
- Member of the Board of the “Int. Graduate School on ICT” of University of Trento.
- Head of the Research Program “*Software Engineering and Formal methods*” at DISI, University of Trento.
- Formerly delegate for student tutoring (“delegato per il tutorato”) at DISI, University of Trento.
- Formerly head of the committee for the Qualifying Exam of the “Int. Graduate School on ICT” of University of Trento.
- Formerly member of the committee for the Admission Exam of the “Int. Graduate School on ICT” of University of Trento.

International visibility

Citation Indexes

Google Scholar: ⁴

- # citations: 8167,
- h-index: 42
- 1 paper with more than 1800 citations, 10 papers with more than 200, 15 papers with more than 100.

Scopus: ⁵

- # citations: 3320,
- h-index: 26

Top Italian Scientists: Occurs in the list of *Top Italian Scientists*.⁶

Ranking of publications

As author, at present date he has published:

- 27 papers on refereed journals,
- 4 book chapters, among which 2 chapters of *Handbook of Satisfiability*;
- 65 papers on refereed conference proceedings, among which 45 of rating *A** “Flagship” (AAAI, CAV, IJCAI, IJCAR, KR) or *A* “Excellent” (CADE, CAISE, ER, FME, LPAR, SAT, TACAS) according to CORE. ⁷
- 8 papers on other refereed non-archival workshops proceedings.

Editing & Membership of Editorial Boards

- (2003-current) Member of the Ed. Board of “*Journal on Boolean Modeling, Reasoning and Computation*”.
- (2006-2018): Associate editor “*Journal on Boolean Modeling, Reasoning and Computation*”. IOS Press.
- (2018-current) Guest co-editor, with D. Garmiche and S. Schulz, of *Journal on Automated Reasoning*, “Special Issue on Selected Extended papers of IJCAR’18”. Springer.
- (2007) Guest co-editor, with B. Cook, of *Journal on Satisfiability, Boolean Modeling and Computation, JSAT*, “Special Issue on Satisfiability Modulo Theories”. IOS Press.
- (2001-02). Guest Co-Editor, with S. Linton, of *Journal of Symbolic Computation: Special Issue on Integration of Automated Reasoning and Computer Algebra Systems*. Elsevier.

Chairmanships & Memberships of Steering and Program Committees

- (2018) Co-chair of *International Joint Conference on Automated Reasoning (IJCAR’18)*. Proc. in LNCS, Springer. (Classified *A** (Flagship) by CORE ⁷.)
- (2012) Co-chair of *Theory and Applications of Satisfiability Testing (SAT’12)*. Proc. in LNCS, Springer. (Classified *A* (Excellent) by CORE ⁷.)
- (2009) Co-chair of *Frontier on Combining Systems - FroCoS’09*. Proc. in LNCS, Springer.
- (2006) Co-chair of *Pragmatics of Decision Procedures in Automated Reasoning - PDPAR’06 (now called “SMT”)*. Proc. in ENTCS, Elsevier.
- (2001) Co-chair of *CALCULEMUS-2001, 9th Symposium on the Integration of Symbolic Computation and Mechanized Reasoning*.
- Member of the Steering Committee of the conferences: SAT, IJCAR, FRODOS, CALCULEMUS.
- Senior Program Committee member of International Joint Conference on Artificial Intelligence (IJCAI)
- Member of the Program Committee of the conferences: AAAI, AIMS, CADE, CAV, FRODOS, IJCAI, IJCAR, KR, PDPAR, SAT, VMCAI, plus some workshops.

⁴http://scholar.google.com/citations?user=qmnmYsAAAAJ&view_op=list_works&pagesize=100.

⁵<http://www.scopus.com/authid/detail.url?authorId=8859979400>

⁶<http://www.topitalianscientists.org>

⁷ *Computer Research & Education International Conference Ranking, CORE*: <http://core.edu.au>.

International collaborations

He has published more than 90 papers with more than 50 coauthors, including world-class scientists like:

- Prof. Edmund Clarke, CMU (ACM Turing Award 2007);
- Prof. Moshe Vardi, Rice Univ, TX (ACM Goedel Prize 2000, ACM Kanellakis Award 2005);
- Prof. Alan Bundy, Univ. of Edimburgh (Herbrand Award, 2007);
- Prof. Ian Horrocks, Oxford Univ. (BCS Needham award 2005);
- Prof. John Mylopoulos, Univ. of Ottawa, CA (Chen Award 2010).
- Prof. Toby Walsh, Camberra; (Humboldt Prize 2014),
- Dr. Ziyad Hanna, Vice President of R&D, Cadence Design Systems;
- Dr. Peter Patel-Schneider, Lucent;
- Prof. Fausto Giunchiglia, University of Trento;
- Dr. Alessandro Cimatti, FBK-IRST.

Awards

- *AWARD “EAAI Top-Cited Article 2005-2010” by Elsevier ([J19])*,
for the paper “Goal-Oriented Requirements Analysis and Reasoning in the Tropos Methodology” by P. Giorgini, J. Mylopoulos, R. Sebastiani, Engineering Application of Artificial Intelligence Journal. Volume 18/2, March 2005. Ed. Elsevier.
- *BEST PAPER AWARD at Formal Methods for Computed-Aided Design conference, 2010 ([C23])*,
for the paper “Applying SMT in Symbolic Execution of Microcode” by A. Franzen, A. Cimatti, A. Nadel, R. Sebastiani, J. Shalev. Lugano, Switzerland, October 2010.
- *Official nomination for Best Paper Award at TACAS’11 conference for the paper “Efficient interpolant generation in satisfiability modulo linear integer algebra” [C22]* by A. Griggio, T.T.H. Le and R. Sebastiani (see <http://www.etaps.org/index.php/2011/best-papers>)

Publications – Editor

Edited Journal Special Issues

- (i) B. Cook, R. Sebastiani, Editors. “Special Issue on Satisfiability Modulo Theories”. *Journal on Satisfiability, Boolean Modeling and Computation*, JSAT. Vol. 3, 2007. IOS Press.
- (ii) S. Linton, R. Sebastiani, Editors. “Special Issue on Integration of Automated Reasoning and Computer Algebra Systems.” *Journal of Symbolic Computation*, Vol 34 (4) October 2002, Elsevier.

Edited Archival Conference Proceedings

- (a) D. Galmiche, S. Schulz, R. Sebastiani, Editors. “Automated Reasoning - IJCAR’18, 9th International Joint Conference”. Oxford, UK, July 14-17, 2018. LNCS/LNAI, vol 10900, Springer.
- (b) A. Cimatti, R. Sebastiani, Editors. “Theory and Applications of Satisfiability Testing - SAT 2012 - 15th International Conference”. Trento, Italy, June 17-20, 2012. Vol. 7317 LNCS, Springer.
- (c) S. Ghilardi, R. Sebastiani, Editors. “Frontiers of Combining Systems, 7th International Symposium, FroCoS 2009”, Trento, Italy, September 16-18, 2009. Vol. 5749 LNCS, Springer.
- (d) B. Cook, R. Sebastiani, Editors. ”Proceedings of the Fourth Workshop on Pragmatics of Decision Procedures in Automated Reasoning (PDPAR 2006)”. ENTCS, Vol 174, (8) June 1007, Elsevier.

(a) and (b) are classified respectively as A* (Flagship) and A (Excellent) by CORE ⁷.

Edited Workshop Proceedings (non archival)

- (a) S. Linton and R. Sebastiani editors. Proc. *CALCULEMUS-2001, 9th Symposium on the Integration of Symbolic Computation and Mechanized Reasoning*. Siena, Italy, 21-22 June 2001.

Publications – Author

All publications are available from <http://disi.unitn.it/rseba/publist.html>.

Papers on Journals

- J1. P. Morettin, A. Passerini, R. Sebastiani “Advanced SMT Techniques for Weighted Model Integration”. *Artificial Intelligence*, vol. 275, 2019. <https://doi.org/10.1016/j.artint.2019.04.003>.
- J2. R. Sebastiani, P. Trentin. “OptiMathSAT: A Tool for Optimization Modulo Theories”. 2018. *Journal of Automated Reasoning - JAR*. Springer. 2018. <https://doi.org/10.1007/s10817-018-09508-6>.
- J3. A. Cimatti, A. Griggio, A. Irfan, M. Roveri, R. Sebastiani “Incremental Linearization for Satisfiability and Verification Modulo Nonlinear Arithmetic and Transcendental Functions”. 2018. *ACM Transactions on Computational Logics, TOCL*. Volume 19 Issue 3, Article 19. August 2018.
- J4. M. C. Nguyen, R. Sebastiani, P. Giorgini, J. Mylopoulos “Multi-Objective Reasoning with Constrained Goal Models”. *Requirements Engineering*, June 2018, Volume 23, Issue 2, pp 189-225.
- J5. S. Teso, R. Sebastiani, A. Passerini. “Structured Learning Modulo Theories”. *Artificial Intelligence*. Volume 244, pages 166-187, March 2017.
- J6. R. Sebastiani and S. Tomasi “Optimization Modulo Theories with Linear Rational Costs” *ACM Transactions on Computational Logics, TOCL*. Volume 16, Issue 2, March 2015.
- J7. A. Griggio, T.T.H. Le, and R. Sebastiani “Efficient Interpolant Generation in Satisfiability Modulo Linear Integer Arithmetic” *Logical Methods in Computer Science*. Volume 8, Issue 3, pages 1-31, August 2012.

- J8. A. Cimatti, A. Griggio and R. Sebastiani “Computing Small Unsatisfiable Cores in Satisfiability Modulo Theories”. *Journal of Artificial Intelligence Research, JAIR*. Volume 40, pages 701-728, April 2011.
- J9. R. Sebastiani, S. Tonetta, M. Y. Vardi “Symbolic Systems, Explicit Properties: on Hybrid Approaches for LTL Symbolic Model Checking”. *International Journal on Software Tools for Technology Transfer (STTT) Springer*. Volume 13, Number 4, p. 319-335 2011.
- J10. A. Cimatti, A. Griggio and R. Sebastiani “Efficient Interpolant Generation in Satisfiability Modulo Theories”. *ACM Transactions on Computational Logics, TOCL*, vol. 12, number 1, October 2010.
- J11. R. Sebastiani, M. Vescovi “Automated Reasoning in Modal and Description Logics via SAT Encoding: the Case Study of $K(m)/ALC$ -Satisfiability” *Journal of Artificial Intelligence Research, JAIR*. Volume 35, June 2009
- J12. R. Bruttomesso, A. Cimatti, A. Franzén, A. Griggio, R. Sebastiani “Delayed Theory Combination vs. Nelson-Oppen for Satisfiability Modulo Theories: a Comparative Analysis.” Extended version. *Annals of Mathematics and Artificial Intelligence*, 55(1-2), pp. 63-99, February 2009. Ed. Springer.
- J13. R. Sebastiani “Lazy Satisfiability Modulo Theories”. *Journal on Satisfiability, Boolean Modeling and Computation, JSAT*, Vol 3, 2007, pag 141-224. IOS Press.
- J14. R. Sebastiani, E. Siengeman, S. Tonetta, M. Vardi “GSTE is partitioned Model Checking”. Extended version. *Formal Methods in System Designs* vol. 31, 2007. pp. 177–196. Kluwer.
- J15. M. Bozzano, R. Bruttomesso, A. Cimatti, T. Junttila, P. van Rossum, S. Ranise, R. Sebastiani “Efficient Theory Combination via Boolean Search” *Information and Computation*, vol. 204 (10), Ottobre 2006. Elsevier.
- J16. M. Bozzano, R. Bruttomesso, A. Cimatti, Z. Hanna, A. Palti, Z. Kashidashvili, R. Sebastiani “Encoding RTL Constructs for MathSAT: a Preliminary Report” *Electronic Notes in Theoretical Computer Science* vol 144, (2), 2006. Ed. Elsevier.
- J17. M. Bozzano, R. Bruttomesso, A. Cimatti, T. Junttila, P. van Rossum, S. Schulz, R. Sebastiani “MathSAT: A Tight Integration of SAT and Mathematical Decision Procedure” *Journal of Automated Reasoning, Volume 35, (1-3), October, 2005*. Ed. Kluwer/Springer.
- J18. G. Audemard, M. Bozzano, A Cimatti, R. Sebastiani “Verifying Industrial Hybrid Systems with MathSAT” *Electronic Notes in Theoretical Computer Science*. Vol 119, No 2, 2005. Ed. Elsevier.
- J19. P. Giorgini, J. Mylopoulos, R. Sebastiani “Goal-Oriented Requirements Analysis and Reasoning in the Tropos Methodology”. *Engineering Application of Artificial Intelligence Journal*. Volume 18/2, March 2005. Ed. Elsevier. **Awarded by Elsevier “EAAI Top-Cited Article 2005-20102”.**
- J20. P. Giorgini, E. Nicchiarelli, J. Mylopoulos, R. Sebastiani “Formal Reasoning Techniques for Goal Models”. 2003. *Journal of Data Semantics*, vol. 1, September 2003. Ed. Springer.
- J21. P. F. Patel-Schneider, R. Sebastiani “A New General Method to Generate Random Modal Formulae for Testing Decision Procedures”. *Journal of Artificial Intelligence Research - JAIR*, Vol. 18, pp. 351-389, Maggio 2003, Ed Morgan Kaufmann.
- J22. F. Giunchiglia e R. Sebastiani “Building decision procedures for modal logics from propositional decision procedures - the case study of modal $K(m)$ ”. *Information and Computation*. Volume 162 (1/2), Ottobre/Novembre 2000, pp. 158–178. Ed. Academic Press.
- J23. E. Giunchiglia, F. Giunchiglia, R. Sebastiani, A. Tacchella. “SAT vs. Translation Based decision procedures for modal logics: a comparative evaluation”. *Journal of Applied Non-Classical Logics - JANCL*. Volume 10 (2), pp. 145-172, Settembre 2000. Ed. Hermès International, Oxford.
- J24. I. Horrocks, P. F. Patel-Schneider, R. Sebastiani. “An Analysis of Empirical Testing for Modal Decision Procedures”. *Logic Journal of the Interest Group in Pure and Applied Logics (IGPL)*. Volume 8, Issue 3, pp. 293–323, Maggio 2000. Ed. Oxford Press.

- J25. A. Bundy, F. Giunchiglia, R. Sebastiani, T. Walsh. “Calculating Criticalities”. *Artificial Intelligence*. Vol. 88, Issue 1-2, pp. 39-67. Dicembre 1996. Ed. Elsevier.
- J26. R. Sebastiani. “Applying GSAT to non-clausal formulas”. *Journal of Artificial Intelligence Research - JAIR*, Vol.1, pp.309-314. Giugno 1994. Ed. Morgan Kauffman.
- J27. R. Sebastiani. “Astrazione: dalla Teoria alla Realizzazione di un Abstract Proof Checker”. *AI*IA Notizie*, n.2, Giugno 1993, pp. 41-53. Ed. S.S. Reiss Romoli.

Book Chapters

- B1. P. Giorgini, J. Mylopoulos, R. Sebastiani “Goal Modelling and Reasoning in TROPOS.” *Social Modeling for Requirements Engineering*. January 2011. Ed. MIT press.
- B2. R. Sebastiani e A. Tacchella “SAT Techniques for Modal and Description Logics” *Part II, Ch. 25, Handbook of Satisfiability*. 2009 Ed. Ios Press.
- B3. C. Barrett, R. Sebastiani, S. Seshia, C. Tinelli. “Satisfiability Modulo Theories” *Part II, Ch. 26, Handbook of Satisfiability*. 2009 Ed. Ios Press.
- B4. A. Cimatti, R. Sebastiani. “Building Efficient Decision Procedures on top of SAT solvers.” *Formal Methods for Hardware Verification*. May 2006. LNCS, 3965. Ed. Springer.

Papers on Archival Conference Proceedings

- C1. S. Kolb, P. Morettin, P. Zuidberg, F. Somnavilla, A. Passerini, R. Sebastiani, L. De Raedt “The pywmi Framework and Toolbox for Probabilistic Inference using Weighted Model Integration” *In proc. Int. Joint Conference on Artificial Intelligence, IJCAI, 2019*.
- C2. P. Trentin, R. Sebastiani “Optimization Modulo the Theory of Floating-Point Numbers” *In proc. The 27th International Conference on Automated Deduction - CADE-27 2019*. LNAI/LNCS, Springer.
- C3. A. Cimatti, A. Griggio, A. Irfan, M. Roveri, R. Sebastiani “Incremental Linearization: A practical approach to Satisfiability Modulo Nonlinear Arithmetic and Transcendental Functions” *In proc. 20th International Symposium on Symbolic and Numeric Algorithms for Scientific Computing, SYNASC’18*. Invited paper.
- C4. E. Cardoso, J. Horkoff, R. Sebastiani and J. Mylopoulos “Planning with Strategic Goals” *In proc. 22nd IEEE Enterprise Distributed Object Computing Conference – EDOC 2018*. IEEE.
- C5. A. Cimatti, A. Griggio, A. Irfan, M. Roveri, R. Sebastiani “Experimenting on Solving Nonlinear Integer Arithmetic with Incremental Linearization” *In proc. 21st International Conference on Theory and Applications of Satisfiability Testing, SAT 2018*. LNCS, vol 10929, Springer.
- C6. Z. Bian, F. Chudak, W. Macready, A. Roy, R. Sebastiani, S. Varotti “Solving SAT and MaxSAT with a Quantum Annealer: Foundations and a Preliminary Report.” *In proc. 11th International Symposium on Frontiers of Combining Systems, FroCoS 2017*. LNCS, vol. 10483, Springer.
- C7. C. M. Nguyen, R. Sebastiani, P. Giorgini, J. Mylopoulos “Modeling and Reasoning on Requirements Evolution with Constrained Goal Models” *In proc. 5th International Conference on Software Engineering and Formal Methods, SEFM 2017*. LNCS vol. 10469, Springer.
- C8. P. Morettin, A. Passerini, R. Sebastiani “Efficient Weighted Model Integration via SMT-Based Predicate Abstraction” *In proc. Int. Joint Conference on Artificial Intelligence, IJCAI, 2017*.
- C9. A. Cimatti, A. Griggio, A. Irfan, M. Roveri, R. Sebastiani “Satisfiability Modulo Transcendental Functions via Incremental Linearization” *In proc. Int. Conference on Automated Deduction, CADE, 2017*. LNCS vol. 10395, Springer.

- C10. R. Sebastiani, P. Trentin “On Optimization Modulo Theories, MaxSMT and Sorting Networks” *In proc. Tools and Algorithms for the Construction and Analysis of Systems, TACAS’17, 2017. LNCS, vol. 10205, Springer.*
- C11. A. Cimatti, A. Griggio, A. Irfan, M. Roveri, R. Sebastiani “Invariant Checking of NRA Transition Systems via Incremental Reduction to LRA with EUF” *In proc. Tools and Algorithms for the Construction and Analysis of Systems, TACAS’17, 2017. LNCS, vol. 10205, Springer.*
- C12. C. M. Nguyen, R. Sebastiani, P. Giorgini, J. Mylopoulos “Requirements Evolution and Evolution Requirements with Constrained Goal Models.” *In proc. ER 2016 - International Conference on Conceptual Modeling.* November 2016.
- C13. R. Sebastiani “Colors Make Theories Hard” *Proc. IJCAR 2016 - International Joint Conference on Automated Reasoning. 2016. July 2016. LNCS, 9706, Springer.*
- C14. A. Irfan, A. Cimatti, A. Griggio, M. Roveri, R. Sebastiani “Verilog2SMV: A Tool for Word-level Verification” *Proc. Design, Automation and Test in Europe, DATE 2016. March 2016.*
- C15. R. Sebastiani, P. Trentin. “OptiMathSAT: A Tool for Optimization Modulo theories.” *Proc. International Conference on Computer-Aided Verification, CAV’15. LNCS, Springer, 2015.*
- C16. R. Sebastiani, P. Trentin. “Pushing the Envelope of Optimization Modulo Theories with Linear-Arithmetic Cost Functions”. *In proc. Tools and Algorithms for the Construction and Analysis of Systems, TACAS 2015. LNCS, Springer, 2015.*
- C17. A. Cimatti, A. Griggio, B. J. Schaafsma, R. Sebastiani “A Modular Approach to MaxSAT Modulo Theories”. *In proc. International Conference on Theory and Applications of Satisfiability Testing, SAT 2013. LNCS, vol. 7962, Springer, 2013.*
- C18. A. Cimatti, A. Griggio, B. J. Schaafsma, R. Sebastiani “The MathSAT 5 SMT Solver”. *In proc. Tools and Algorithms for the Construction and Analysis of Systems, TACAS’13. LNCS, vol. 7795, Springer, 2013.*
- C19. R. Sebastiani and S. Tomasi. “Optimization in SMT with LA(Q) Cost Functions” *International Joint Conference on Automated Reasoning - IJCAR12. Manchester, July 2012. Vol. 7317, LNAI series, Springer.*
- C20. A. Griggio, Q.S. Phan, S. Tomasi, and R. Sebastiani “Stochastic Local Search for SMT: Combining Theory Solvers with WalkSAT”. *Proc. FroCoS 2011 - 8th International Symposium on Frontiers of Combining Systems 2011. Vol. 6989, LNAI, Springer.*
- C21. V. Haarslev, R. Sebastiani and M. Vescovi “Automated Reasoning in ALCQ via SMT”. *Proc. International Conference on Automated Deduction, CADE’11. 2011. Vol. 6803 LNAI, Springer.*
- C22. A. Griggio, Thi Thieu Hoa Le, R. Sebastiani “Efficient Interpolant Generation in Satisfiability Modulo Linear Integer Arithmetic”. *In proc. Tools and Algorithms for the Construction and Analysis of Systems, TACAS’11. LNCS, Springer, 2011. Nominated for “Best-paper Award.”*
- C23. A. Franzen, A. Cimatti, A. Nadel, R. Sebastiani, J. Shalev “Applying SMT in Symbolic Execution of Microcode” *In proc. Int. Conference on Formal Methods in Computer Aided Design (FMCAD’10). Lugano, Switzerland, October 2010. Awarded by “Best-paper Award.”*
- C24. A. Cimatti, A. Franzen, A. Griggio, R. Sebastiani, and C. Stenico “Satisfiability Modulo the Theory of Costs: Foundations and Applications” *In proc. Tools and Algorithms for the Construction and Analysis of Systems, TACAS’10. 2010.*
- C25. D. Beyer, A. Cimatti, A. Griggio, E. Keremoglu, R. Sebastiani “Software Model Checking via Large-Block Encoding” *In proc. Int. Conference on Formal Methods in Computer Aided Design (FMCAD’09). Austin, TX, USA, November 2009.*
- C26. A. Cimatti, A. Griggio, R. Sebastiani “Interpolant Generation for UTVPI”. *Atti International Conference on Automated Deduction, CADE’09. Montreal, Canada, August 2009. LNCS, Vol. 5663 Springer.*

- C27. R. Sebastiani, M. Vescovi “Axiom Pinpointing in Lightweight Description Logics via Horn-SAT Encoding and Conflict Analysis”. *Atti International Conference on Automated Deduction, CADE’09*. Montreal, Canada, August 2009. LNCS, Vol. 5663 Springer.
- C28. R. Bruttomesso, A. Cimatti, A. Franzen, A. Griggio, R. Sebastiani “The MathSAT 4 SMT solver”. *Proc. International Conference on Computer-Aided Verification, CAV 2008*. July 2008, Princeton, USA. LNCS, Vol. 5123, Springer.
- C29. A. Cimatti, A. Griggio and R. Sebastiani. “Efficient Interpolant Generation in Satisfiability Modulo Theories” *Proc. Tools and Algorithms for the Construction and Analysis of Systems, TACAS’08* 22-29 March, 2008, York, UK. LNCS, Vol. 4963, Springer.
- C30. R. Sebastiani. “From KSAT to Delayed Theory Combination: Exploiting DPLL Outside the SAT Domain”. Invited Lecture. *Proc. FroCoS 2007 - 6th International Symposium on Frontiers of Combining Systems* Liverpool, UK, September 10-12 2007. LNAI, volume 4720, Springer.
- C31. R. Bruttomesso and A. Cimatti and A. Franzen and A. Griggio and Z. Hanna and A. Nadel and A. Palti and R. Sebastiani. “A Lazy and Layered SMT(BV) Solver for Hard Industrial Verification Problems.” *Proc. Int. Conf. on Computer-Aided Verification, CAV’07, LNCS, volume 4590 Springer*. Berlin, Germany, August 2007.
- C32. A. Cimatti, A. Griggio, R. Sebastiani. “A Simple and Flexible Way of Computing Small Unsatisfiable Cores in Satisfiability Modulo Theories.” *Proc. Tenth International Conference on Theory and Applications of Satisfiability Testing, SAT 2007*. May 28 - 31 2007, Lisbon, Portugal. LNCS, volume 4501, Ed.Springer.
- C33. R. Sebastiani, S. Tonetta, M. Vardi “Property-Driven Partitioning for Abstraction Refinement”. In *proc. Thirteenth International Conference on Tools and Algorithms for the Construction and Analysis of Systems - TACAS’07*. LNCS, volume n. 4424, Ed.Springer.
- C34. R. Bruttomesso, A. Cimatti, A. Franzén, A. Griggio, R. Sebastiani “Delayed Theory Combination vs. Nelson-Oppen for Satisfiability Modulo Theories: a Comparative Analysis.” *13th International Conference on Logic for Programming, Artificial Intelligence and Reasoning - LPAR’06*. Phnom Penh, Cambodia, November 2006. LNAI, n. 4246, Ed. Springer.
- C35. R. Bruttomesso, A. Cimatti, A. Franzén, A. Griggio, A. Santuari, R. Sebastiani “To Ackermann-ize or not to Ackermann-ize? On Efficiently Handling Uninterpreted Function Symbols in $SMT(\mathcal{EUF} \cup \mathcal{T})$.” *13th International Conference on Logic for Programming, Artificial Intelligence and Reasoning - LPAR’06*. Phnom Penh, Cambodia, November 2006. LNAI, n. 4246. Ed. Springer.
- C36. R. Sebastiani, M. Vescovi “Encoding the satisfiability of modal and description logics into SAT: the case study of $K(m)/ALC$ ”.” *Proc. 9th International Conference on Theory and Applications of Satisfiability Testing (SAT’06)*. Seattle, USA, Agosto 2006. LNCS n. 4121, Ed. Springer.
- C37. M. Bozzano, R. Bruttomesso, A. Cimatti, T. Junttila, P. van Rossum, S. Schulz, R. Sebastiani “The MathSAT 3 System” *Proc. CADE-20, Int. Conference on Automated Deduction*. Tallinn, Estonia, 2005. vol. 3632. Ed.Springer.
- C38. M. Bozzano, R. Bruttomesso, A. Cimatti, T. Junttila, P. van Rossum, S. Ranise, R. Sebastiani “Efficient Satisfiability Modulo Theories via Delayed Theory Combination” *Proc. Int. Conf. on Computer-Aided Verification, CAV 2005*. Edinburgh, Scotland. LNCS, vol. 3576. Ed.Springer.
- C39. R. Sebastiani, S. Tonetta, M. Vardi “Symbolic Systems, Explicit Properties: on Hybrid Approaches for LTL Symbolic Model Checking” *Proc. Int. Conf. on Computer-Aided Verification, CAV 2005*. Edinburgh, Scotland. LNCS, vol. 3576. Ed.Springer.
- C40. M. Bozzano, R. Bruttomesso, A. Cimatti, T. Junttila, P. van Rossum, S. Schulz, R. Sebastiani “An Incremental and Layered Procedure for the Satisfiability of Linear Arithmetic Logic” *Proc. TACAS2005, Tools and Algorithms for the Construction and Analysis of Systems*. Edinburgh, Scotland. LNCS, vol. 3440. Ed.Springer.

- C41. R. Sebastiani, E. Siengeman, S. Tonetta, M. Vardi “GSTE is partitioned Model Checking”. 2004. *Proc. 16th Int. Conf. on Computer Aided Verification- CAV’04*. LNCS series, vol. 3114. Springer.
- C42. R. Sebastiani, P. Giorgini, J. Mylopoulos “Simple and Minimum-Cost Satisfiability for Goal Models”. 2004. *Proc. Int. conference on Advanced Information Systems Engineering, CAISE’04*. Riga, Latvia, June 2004. LNCS series, No.3084. Ed. Springer.
- C43. R. Sebastiani and S. Tonetta ““More Deterministic” vs. “Smaller” Büchi Automata for Efficient LTL Model Checking”. *Proc. 2th Advanced Research Working Conference on Correct Hardware Design and Verification Methods (CHARME’03)*. L’Aquila, Italia, 2003. LNCS series, n. 2860. Ed. Springer.
- C44. G. Audemard, A. Cimatti, A. Kornilowicz, R. Sebastiani “Bounded Model Checking for Timed Systems.” *Proc. In Proc. 22nd Joint International Conference on Formal Techniques for Networked and Distributed Systems —FORTE 2002*. Houston, TX, USA, November 2002. LNCS series, n. 2529. Ed. Springer.
- C45. P. Giorgini, E. Nicchiarrelli, J. Mylopoulos, R. Sebastiani “Reasoning with Goal Models” *Proc. Int. Conference of Conceptual Modeling – ER2002* Tampere, Finland, October 2002. n. 2503 LNCS series. Ed. Springer.
- C46. A. Cimatti, E. Clarke, E. Giunchiglia, F. Giunchiglia, M. Pistore, M. Roveri, R. Sebastiani, A. Tacchella “NuSMV 2: An OpenSource Tool for Symbolic Model Checking”. *Proc. Int. Conference of Computer-Aided verification, CAV’02*. Copenhagen, Denmark, July 2002. N. 2404 LNCS series. Ed. Springer.
- C47. G. Audemard, P. Bertoli, A. Cimatti, A. Kornilowicz, R. Sebastiani “A SAT Based Approach for Solving Formulas over Boolean and Linear Mathematical Propositions ”. In *proc. 18th Int. Conference on Automated DEduction, CADE’02* . Copenhagen, Denmark, July 2002. LNAI series, n. 2392 Ed. Springer.
- C48. G. Audemard, P. Bertoli, A. Cimatti, A. Kornilowicz, R. Sebastiani “Integrating Boolean and Mathematical Solving: Foundations, Basic Algorithms and Requirements”. *Proc. Artificial Intelligence, Automated Reasoning, and Symbolic Computation. Proc. of Joint AISC 2002 and Calculemus 2002 conferences*. Marseille, France, June 2002. LNAI series, N.2385. Ed. Springer.
- C49. A. Cimatti, E. Giunchiglia, M. Pistore, M. Roveri, R. Sebastiani, A. Tacchella “Integrating BDD-based and SAT-based Symbolic Model Checking”. *Proc. Frontiers of Combinig Systems, FROCOS’2002*. S. Margherita Ligure, Italy, April 2002. LNAI series, N. 2309. Ed. Springer.
- C50. A. Cimatti, M. Pistore, M. Roveri, R. Sebastiani “Improving the Encoding of LTL Model Checking into SAT”. *Proc. Verification, Model Checking and Abstract Interpretation, VMCAI’02*. Venice, Italy, January 2002. LNCS series, N. 2294. Ed. Springer.
- C51. P. F. Patel-Schneider, R. Sebastiani “A System and Methodology for Generating Random Modal Formulae ”. *Proc. IJCAR-2001, International Joint Conference on Automated reasoning* Siena, Italy, July 2001. LNAI series, N. 2083. Ed. Springer.
- C52. R. Sebastiani, A. Tomasi, F. Giunchiglia. “Model Checking Syllabi and Student Careers.” *Proc. International conference on Tools and Algorithms for the Construction and Analysis of Systems – TACAS2001*. Genova, Italy, April 2001. LNCS series, N. 2031. Ed. Springer.
- C53. F. Giunchiglia, R. Sebastiani, P. Traverso “Combining SAT deciders with domain-dependent reasoners”. *Symbolic Computation and Automated Reasoning*, 2001. Ed. A.K. Peters.
- C54. E. Giunchiglia, R. Sebastiani. “Applying the Davis-Putnam procedure to non-clausal formulas” In *Advances in Artificial Intelligence. Proc. International Conference of the Italian Association for Artificial Intelligence - AI*IA’99*. Bologna, Italy, Settembre 1999. LNAI series, N.1792. Ed. Springer.
- C55. A. Cimatti, P. Pieraccini, R. Sebastiani, P. Traverso, A. Villafiorita. “Formal specification and validation of a vital protocol” *Proc. World Congress on Formal Methods 1999 - FM’99*. Toulouse, Francia, Settembre 1999. LNCS series, N. 1709. Ed. Springer.

- C56. A. Chiappini, A. Cimatti, C. Porzia, G. Rotondo, R. Sebastiani, P. Traverso, A. Villafiorita. “Formal Specification and Development of a Safety-Critical Train Management System” Proc. *International Conference Computer Safety, Reliability and Security - SAFECOMP’99*. Toulouse, Francia, Settembre 1999. LNCS series, N. 1698. Ed. Springer.
- C57. R. Sebastiani, A. Villafiorita “SAT-based decision procedures for normal modal logics: a theoretical framework”. Proc. *8th International Conference on Artificial Intelligence: Methodology, Systems, Applications - AIMS’98*. Sozopol, Bulgaria, Settembre 1998. LNAI series, N. 1480. Ed. Springer.
- C58. E. Giunchiglia, A. Massarotto e R. Sebastiani. “Act, and the rest will follow – exploiting determinism in planning as satisfiability.”. Proc. *15th National Conference on Artificial Intelligence - AAAI’98*. Madison, Wisconsin. Luglio 1998. Ed. AAAI Press.
- C59. E. Giunchiglia, F. Giunchiglia, R. Sebastiani e A. Tacchella “More evaluation of decision procedures for modal logics”. Proc. *6th International Conference on Principles of Knowledge Representation and Reasoning - KR’98*. Trento, Italia. Giugno 1998. Ed. Morgan Kauffman.
- C60. F. Giunchiglia, M. Roveri e R. Sebastiani “A new method for testing decision procedures in modal logics”. *14th International Conference on Automated Deduction - CADE14* - LNAI series, N.1249. Townsville, Australia. Luglio 1997. Ed. Springer.
- C61. F. Giunchiglia e R. Sebastiani “A SAT-based decision procedure for ALC”. Proc. *5th International Conference on Principles of Knowledge Representation and Reasoning - KR’96*. Cambridge, MA, USA, November 96. Ed. AAAI Press.
- C62. A. Bundy, F. Giunchiglia, R. Sebastiani, T. Walsh. “Computing Abstraction Hierarchies by Numerical Simulation”. Proc. *14th National Conference on Artificial Intelligence - AAAI96*. Portland, Oregon, USA, Agosto 1996. Ed. AAAI Press/ The MIT Press.
- C63. F. Giunchiglia e R. Sebastiani “Building decision procedures for modal logics from propositional decision procedures - the case study of modal K”. Proc. *13th International Conference on Automated Deduction - CADE13* - LNAI series, N.1104. New Brunswick, New Jersey, USA. Luglio 1996. Ed. Springer.
- C64. F.Giunchiglia, R.Sebastiani, A.Villafiorita, T.Walsh. “A General Purpose Reasoner for Abstraction”. Proc. *Canadian Conference on Artificial Intelligence - AI96*. Toronto, Canada. Maggio 1996. LNAI series, N. 1081. Ed. Springer.
- C65. R. Sebastiani, A. Villafiorita, F. Giunchiglia “Proving Theorems by Using Abstraction Interactively”. In *Trends in Theoretical Informatics*, Innsbruck, 1996. Ed. Die Deutsche Bibliothek.

Other Papers on Peer-reviewed Non-Archival Workshop Proceedings

- W1. A. Irfan, A. Cimatti, A. Griggio, M. Roveri, R. Sebastiani “Lemmas for Satisfiability Modulo Transcendental Functions via Incremental Linearization” Proc. *Int. Workshop on Satisfiability Checking and Symbolic Computation, SC² 2019*. Bern, Switzerland, 2019.
- W2. J. H. Davenport, M. England, R. Sebastiani, P. Trentin “OpenMath and SMT-LIB” *OpenMath 2017 Workshop, at CICM 2017*, Edinburgh, UK, 2017.
- W3. R. Sebastiani and P. Trentin. “On the benefits of enhancing optimization modulo theories with sorting networks for MaxSMT” 2016 Proc. *SMT 2016 Workshop*. CEUR Workshop Proceedings
- W4. S. Teso, R. Sebastiani, A. Passerini “Constructive Learning Modulo Theories.” *ICML Workshop on Constructive Machine Learning*. Lille, France, July 10, 2015
- W5. S. Teso, R. Sebastiani, A. Passerini “Hybrid SRL with Optimization Modulo Theories.” *NIPS Workshop on Constructive Machine Learning*. Lake Tahoe, 2013
- W6. F Giunchiglia, M Roveri, R Sebastiani “A new method for testing decision procedures in modal and terminological logics” *Int. Workshop on Description Logics, 1996*

- W7. A. Villafiorita, R. Sebastiani “Proof Planning by Abstraction.” *ECAI-94 Workshop 'From Theorem Provers to Mathematical Assistants: Issues and Possible Solutions*. 1994.
- W8. R. Sebastiani, M. Di Manzo, F. Giunchiglia “Using abstraction interactively.” *Proceedings of the Workshop on Theory Reformulation and Abstraction*. Jackson Hole, Wyo., 22-24 May 1994

Theses

- T1. R. Sebastiani. “Una nuova classe di procedure di decisione per logiche modali e terminologiche: teoria, implementazione e testing”. - Tesi di Dottorato. DIST, Università di Genova, Febbraio 1997.
- T2. R. Sebastiani. “L’astrazione in un dimostratore automatico interattivo: definizione e realizzazione”. ITC-IRST Technical Report 9112-24 - Tesi di Laurea. Università di Padova, Dicembre 1991.